

IGT30

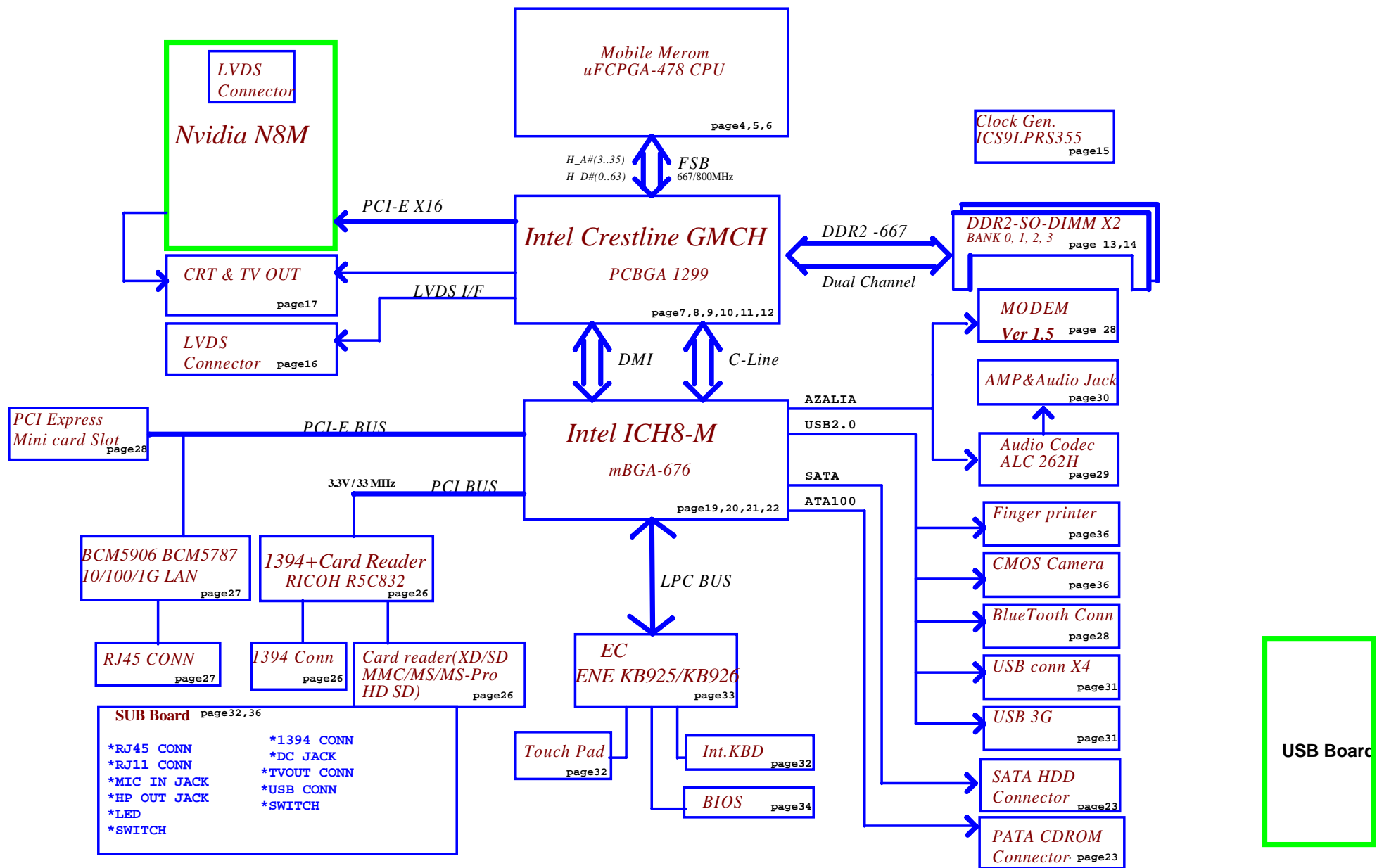
Schematics Document

Mobile Merom uFCPGA with Intel
Crestline_GM/PM+ICH8-M core logic

Monday, December 25, 2006

REV:0.1

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Voltage Rails

O MEANS ON X MEANS OFF

power plane State	+B LDO3 LDO5	+5VALW +3VALW	+1.8V +5V +0.9V	+5VS +3VS +2.5VS +1.8VS +1.5VS +1.25VS +VGA_CORE +CPU_CORE +VCCP	CLOCK
S0	O	O	O	O	O
S3	O	O	O	X	O
S5 S4/AC	O	O	X	X	O
S5 S4/ Battery only	O	X	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X	X

O MEANS ON
X MEANS OFF

S3 : STR
S4 : STD
S5 : SOFT OFF

External PCI Devices

Device	IDSEL#	REQ#/GNT#	Interrupts
1394	AD22	0	PIRQG/H

EC SM Bus1 address

EC SM Bus2 address

Device	Address	Device	Address
Smart Battery	0001 011X b	ADM1032	1001 100X b
EEPROM(24C16/02)	1010 000X b		

ICH8 SM Bus address

Device	Address
Clock Generator (ICS954226)	1101 001Xb
DDRII DIMM0	1010 000Xb
DDRII DIMM1	1010 010Xb

SKU ID Table

Vcc	3.3V +/- 5%			
Ra	100K +/- 5%			
Board ID	Rb	VAD_BID min	VAD_BID typ	VAD_BID max
* 0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

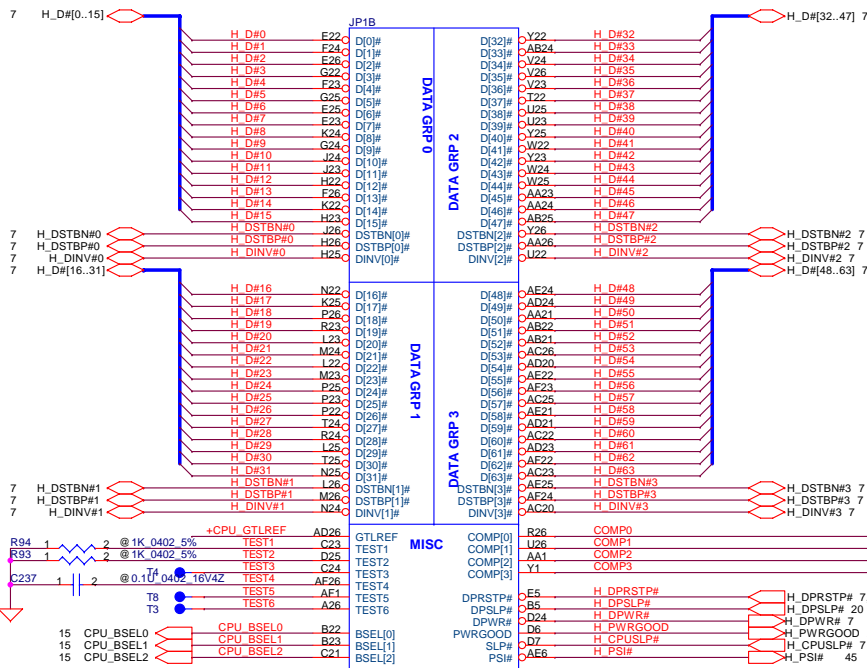
BOM Structure

MARK	FUNCTION
@	NC FOR ALL
GIGA@	BCM5787
100@	BCM5906
UMA@	Internal 965GM
VGA@	965PM + Ext VGA

Address

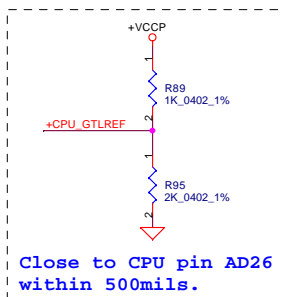
USB PORT LIST

PORT	DEVICE
0	LEFT SIDE
1	WIRELESS
2	RIGHT SIDE
3	CMOS
4	RIGHT SIDE
5	NEW CARD
6	
7	BT(HDL20)
8	
9	3G

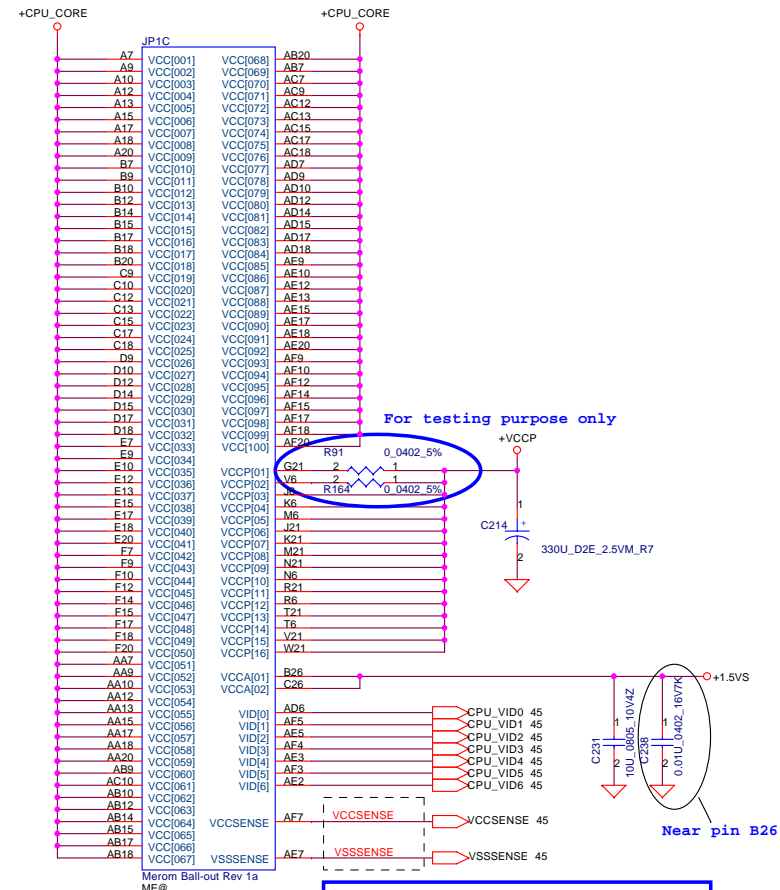


layout note: Route TEST3 & TEST5 traces on ground referenced layer to the TPs

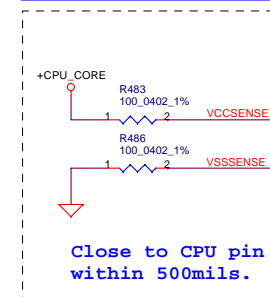
CPU_BSEL	CPU_BSEL2	CPU_BSEL1	CPU_BSEL0
166	0	1	1
200	0	1	0

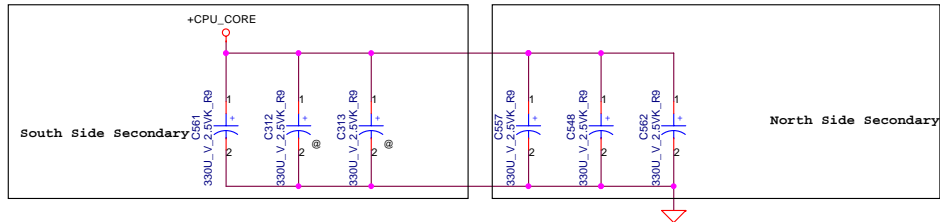
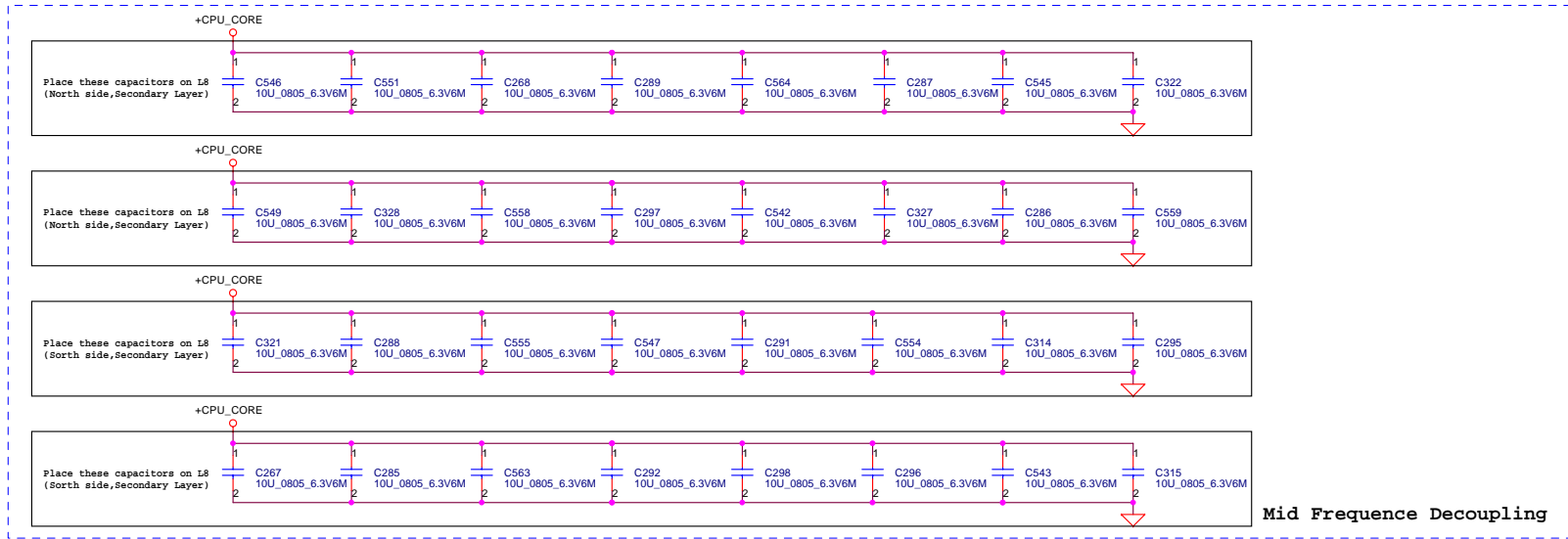
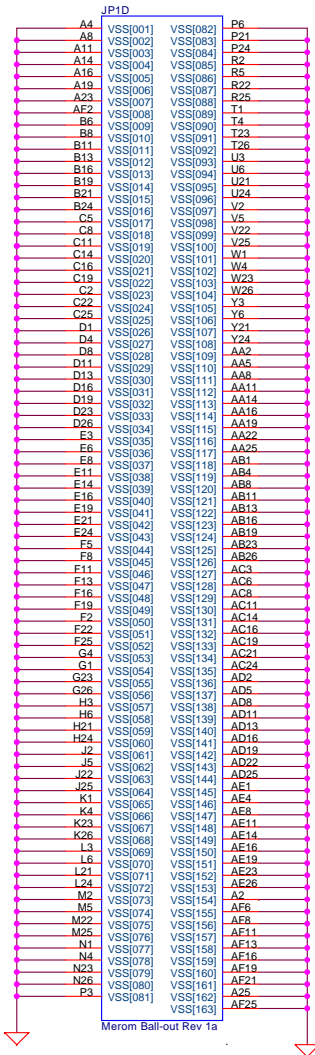


Resistor placed within 0.5" of CPU pin. Trace should be at least 25 mils away from any other toggling signal. COMP[0,2] trace width is 18 mils. COMP[1,3] trace width is 4 mils.

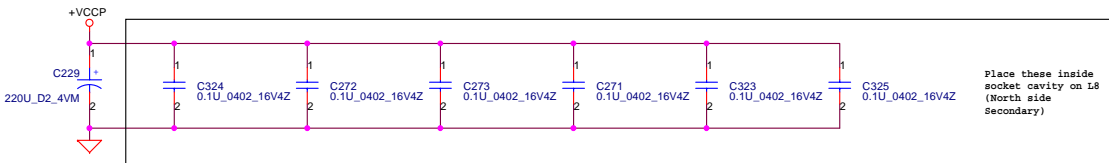


Length match within 25 mils. The trace width/space/other is 20/7/25.

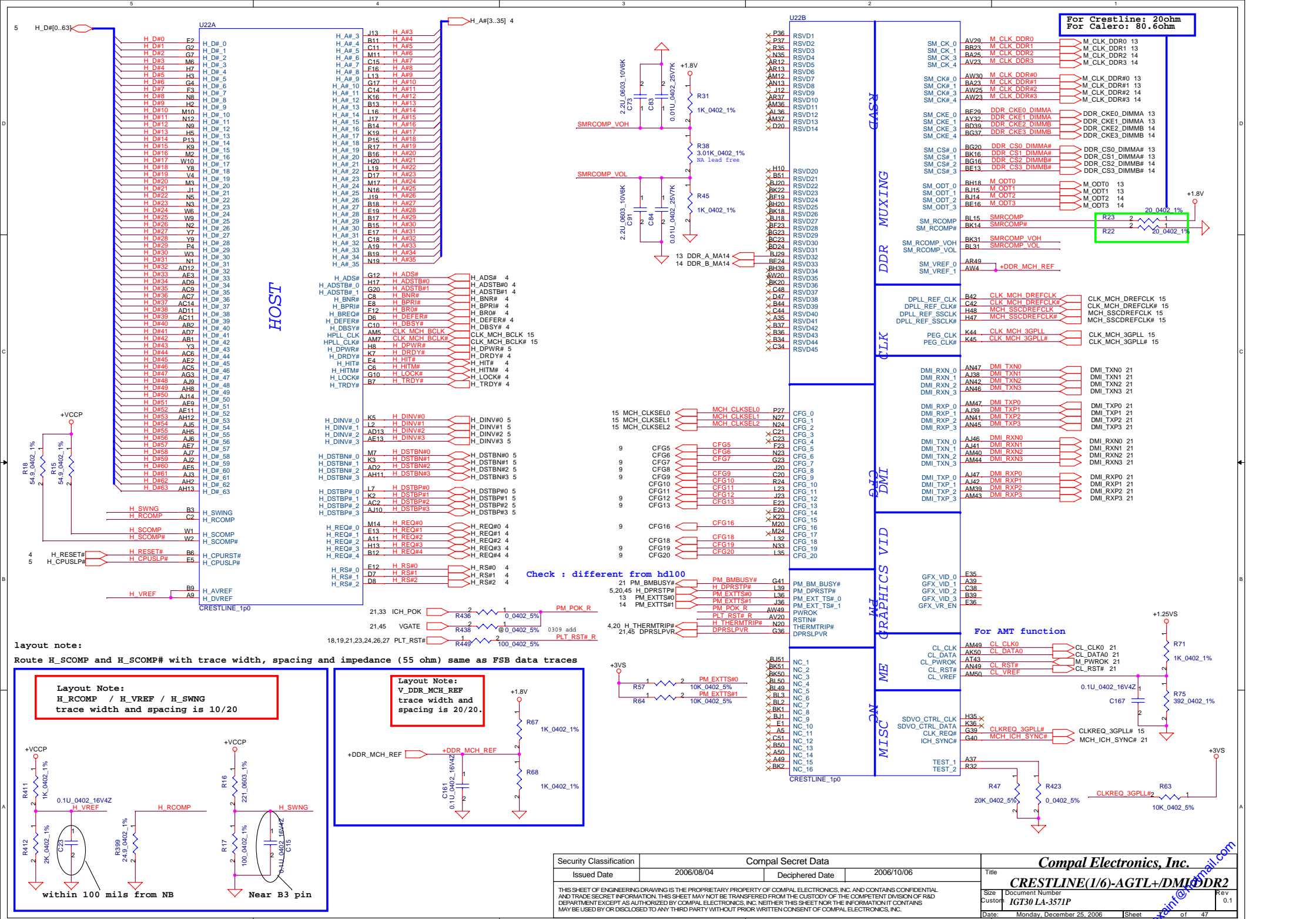




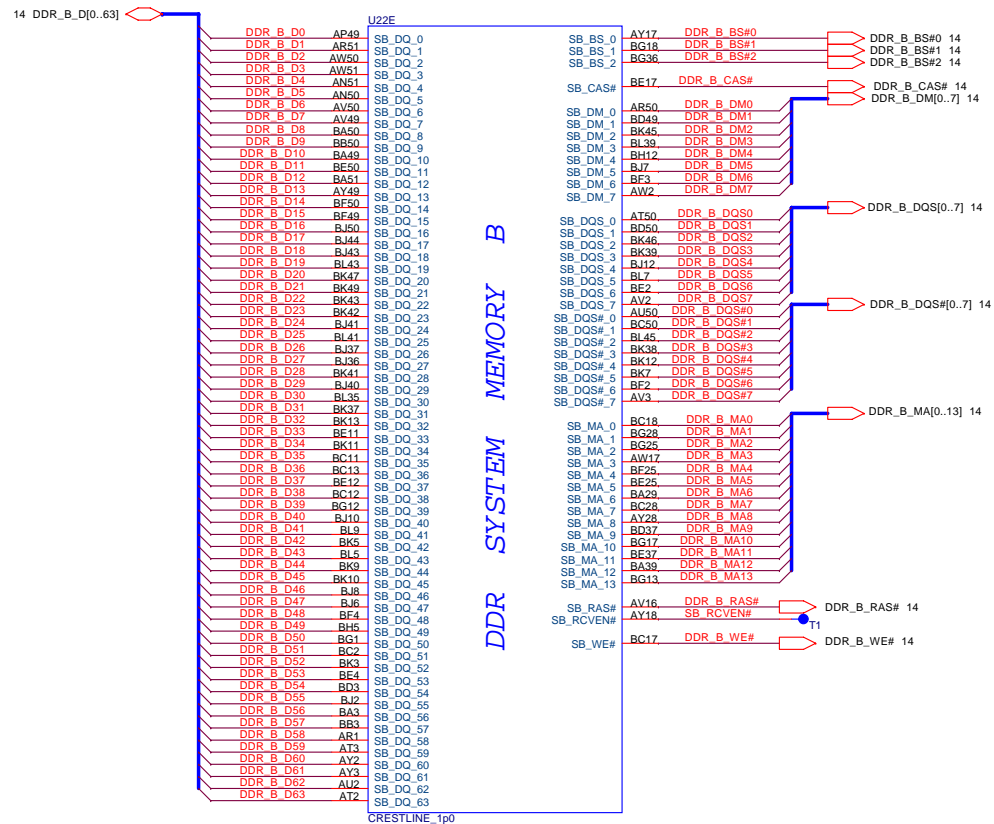
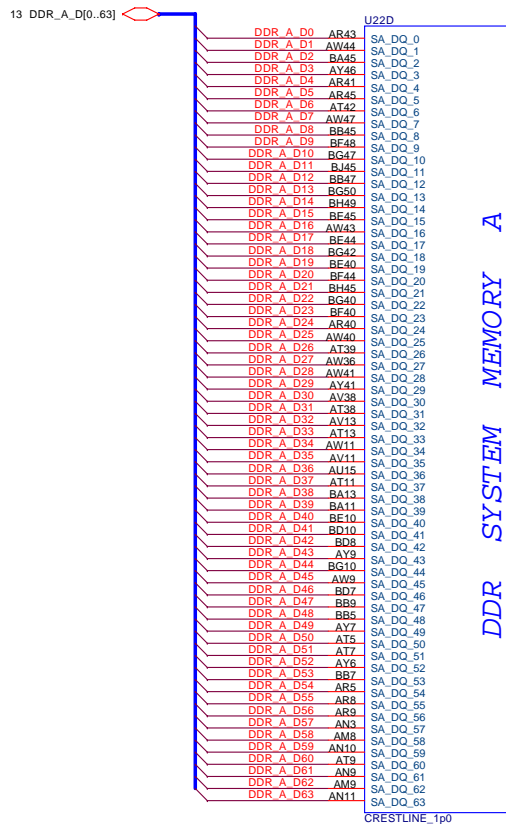
ESR <= 1.5m ohm
Capacitor > 1980uF

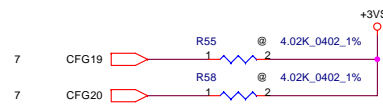
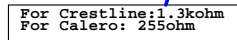


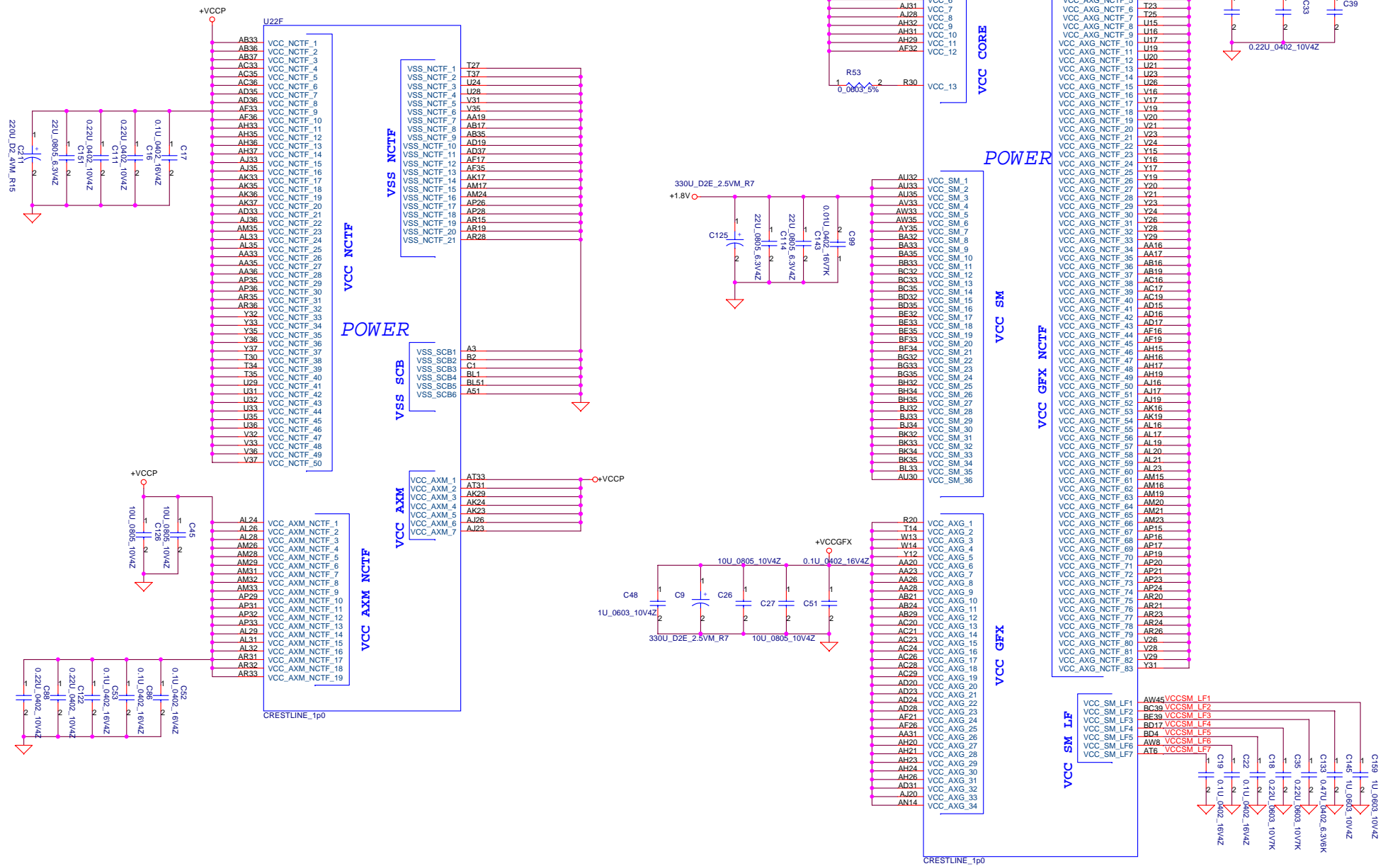
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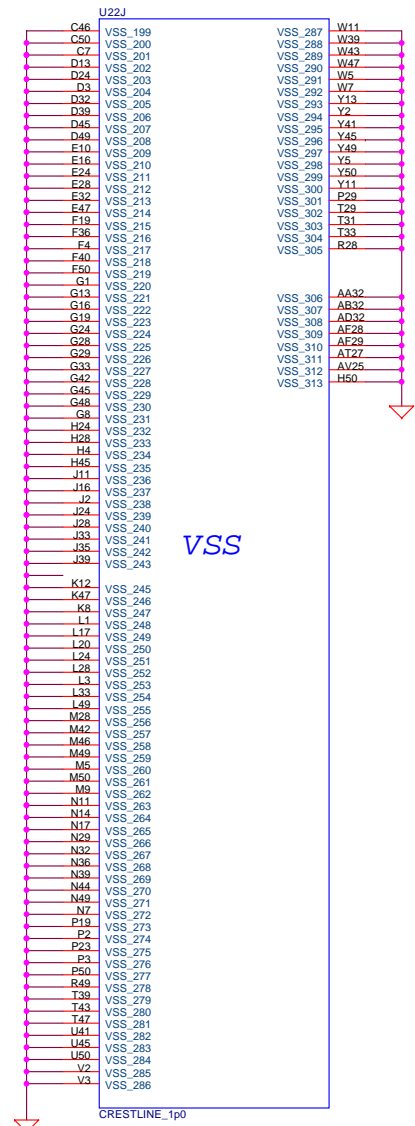
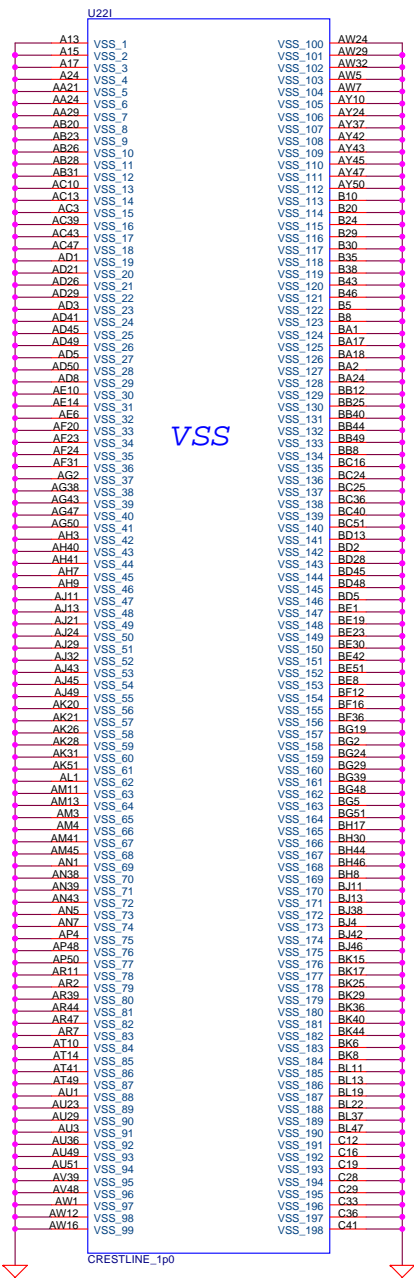


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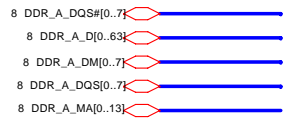






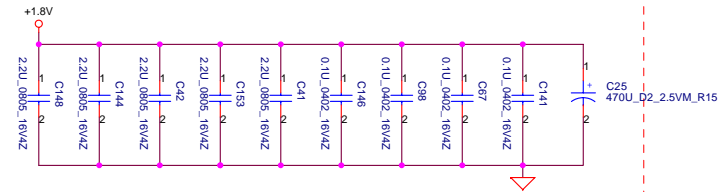


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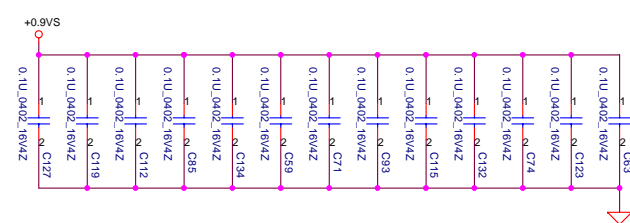


Layout Note:
+DDR_MCH_REF
trace width and
spacing is 20/20.

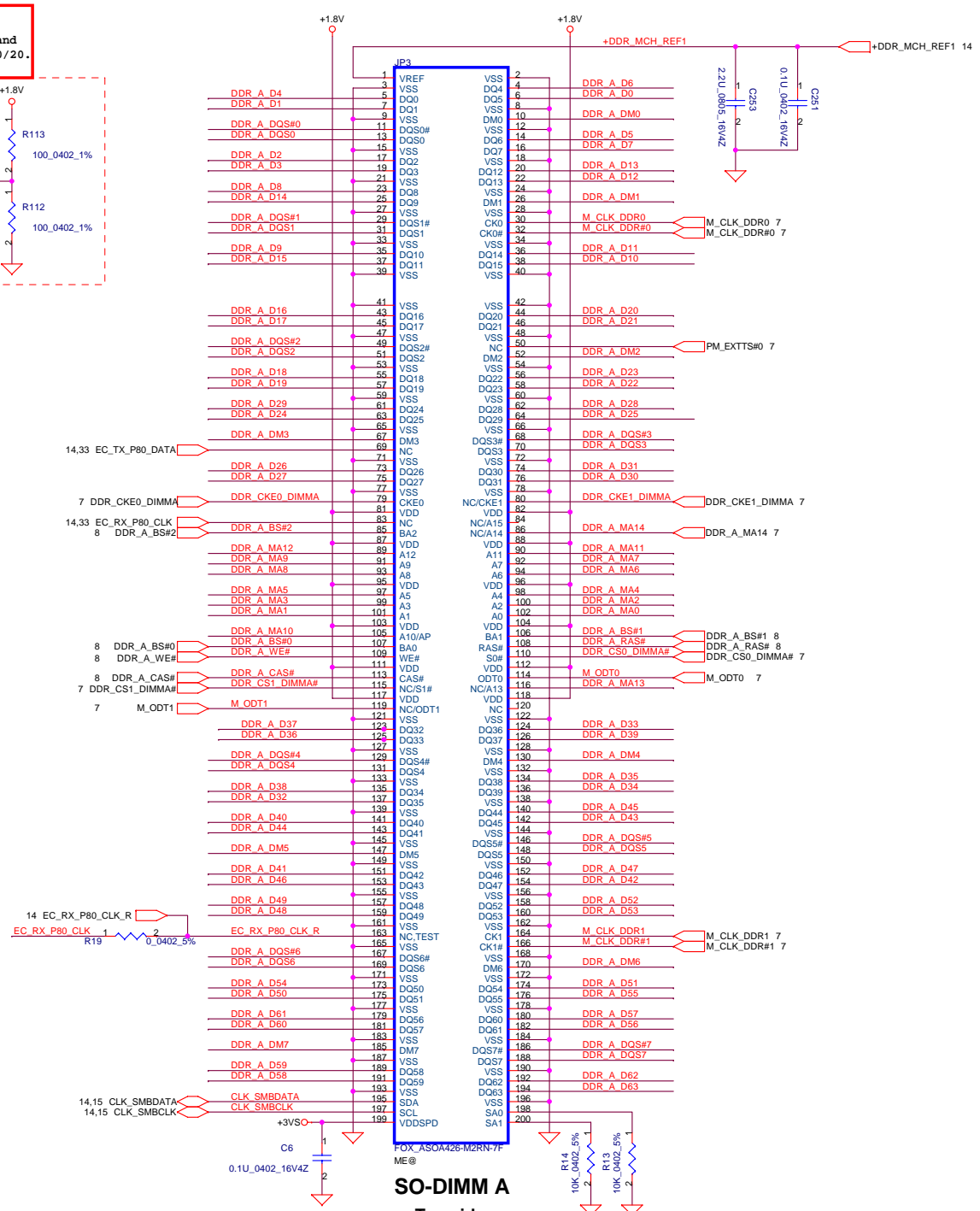
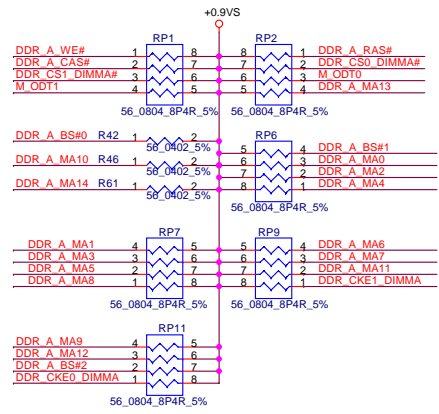
Layout Note:
Place near JP41



Layout Note:
Place one cap close to every 2 pullup
resistors terminated to +0.9VS

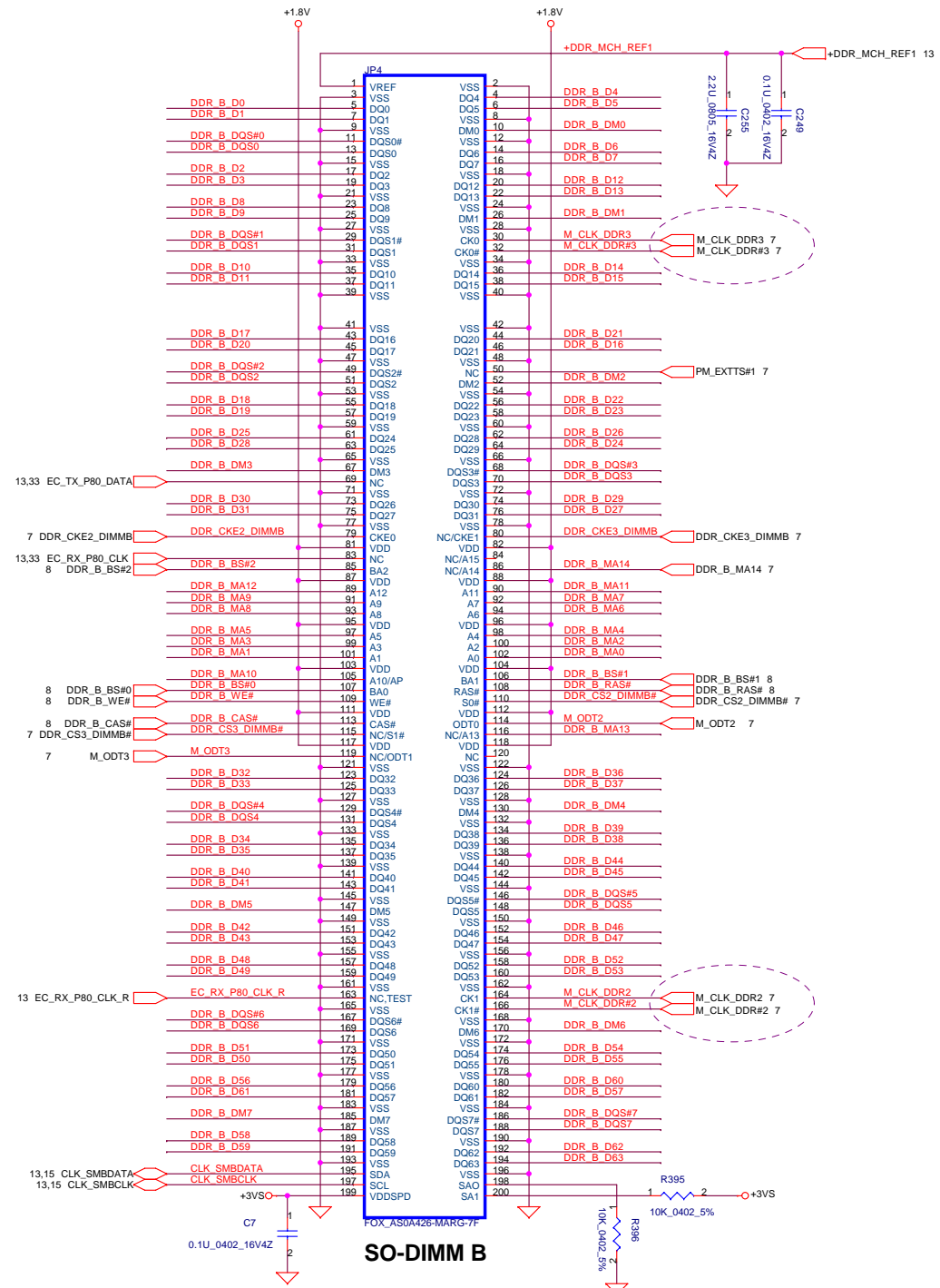
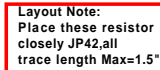
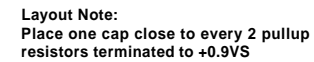
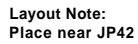


Layout Note:
Place these resistor
closely JP41, all
trace length Max=1.5"

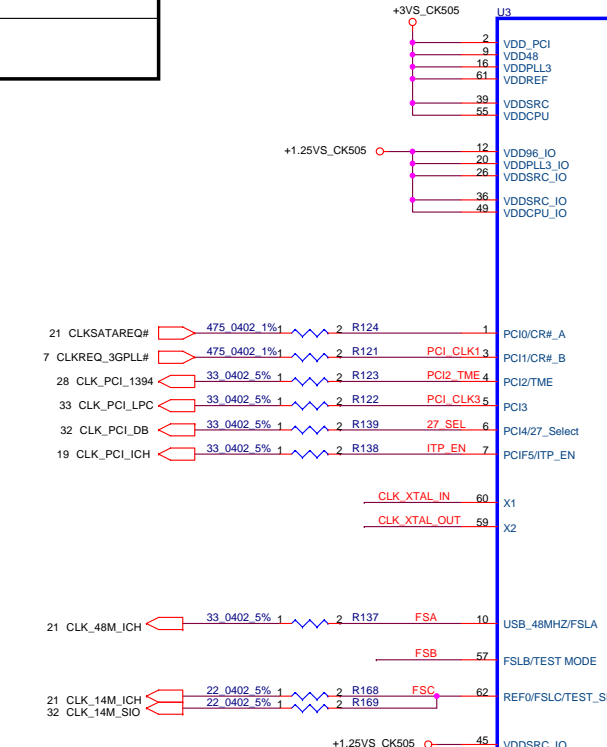
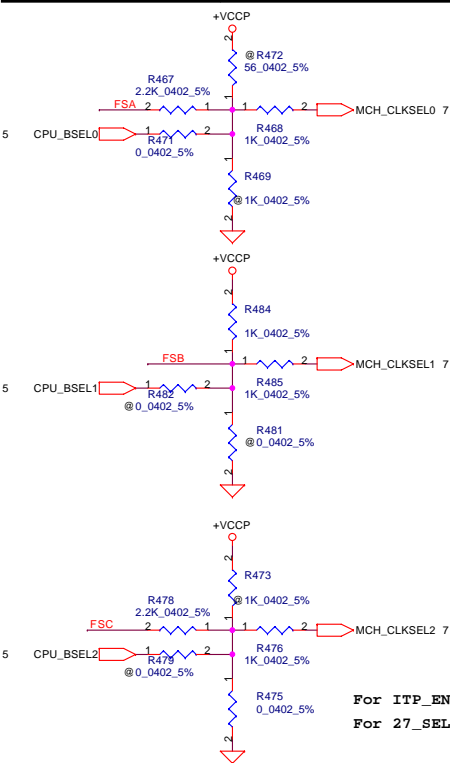
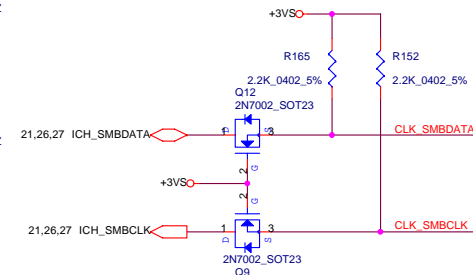
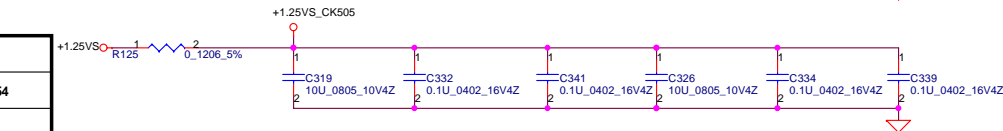


SO-DIMM A
Top side

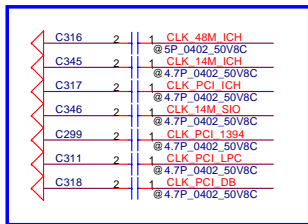
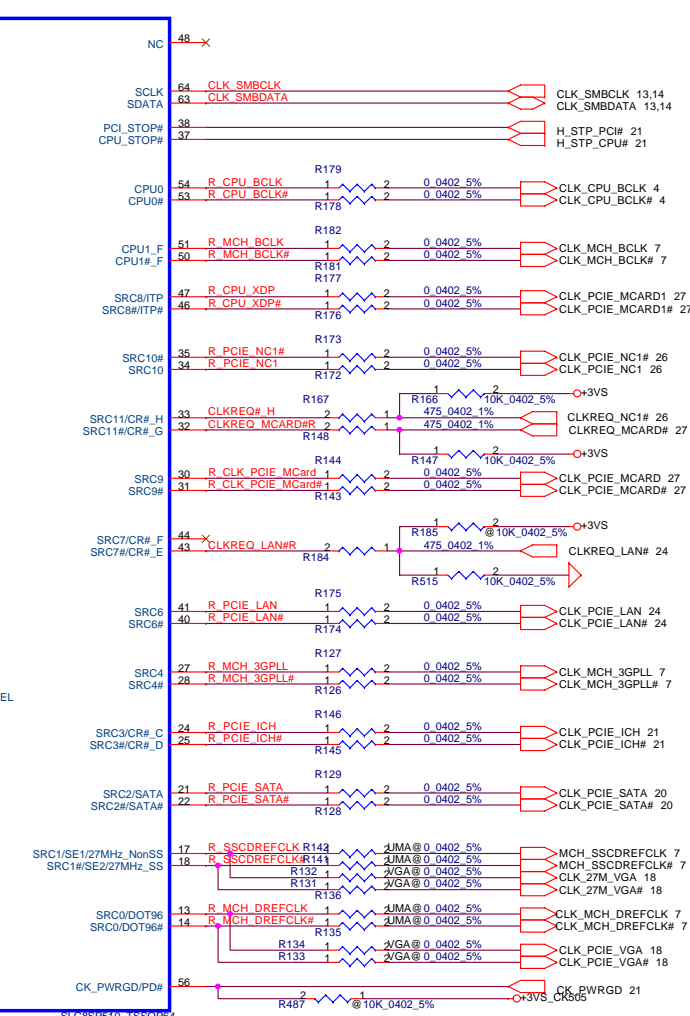
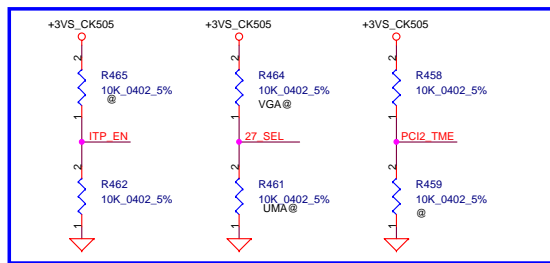
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FSB Frequency Selet:

```
For ITP_EN, 0 =SRC8/SRC8#; 1 = ITP/ITP#
For 27_SEL, 0 = Enable DOT96 & SRC1,
            1= Enable SRC0 & 27MHz
For PCI2_EN, 0 = Overclocking of CPU and SRC Allowed
            1 = Overclocking of CPU and SRC NOT allowed
```

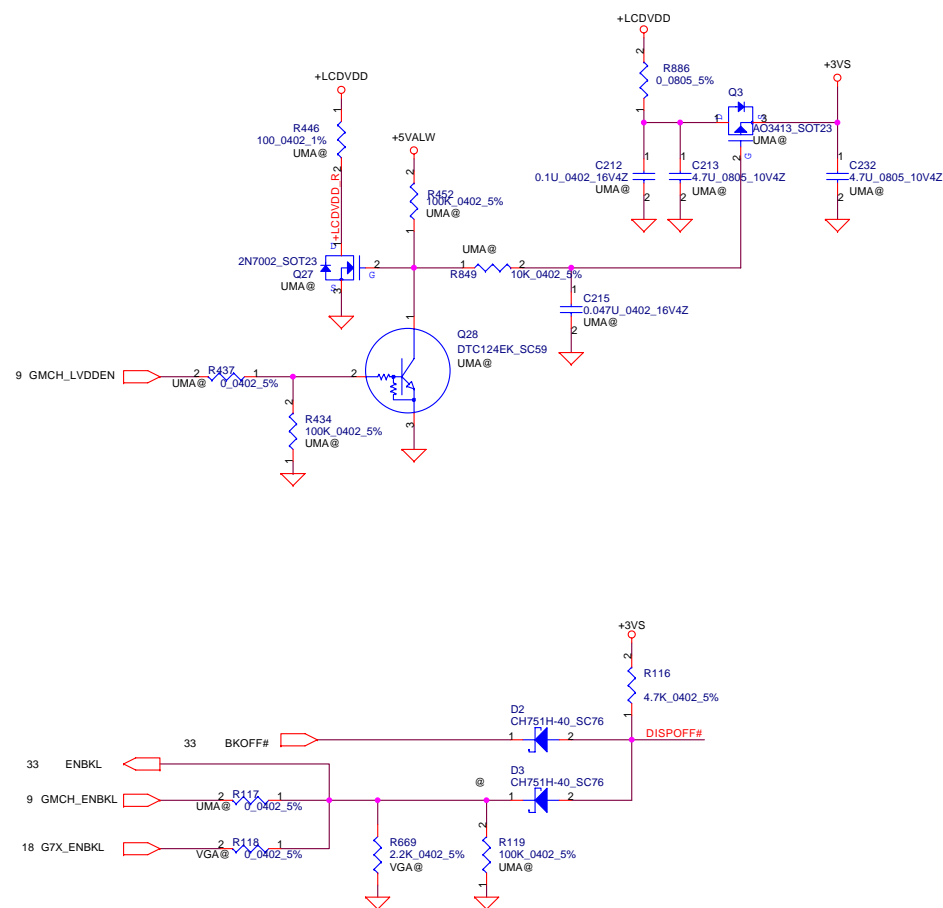


Place close to U41

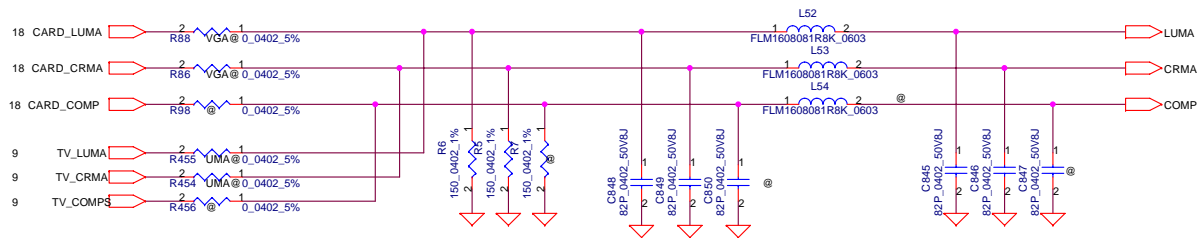
Routing the trace at least 10mil

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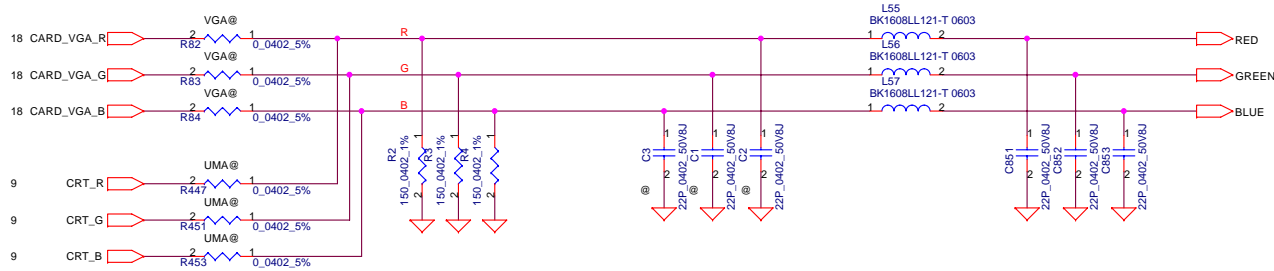
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Title			
Clock generator			
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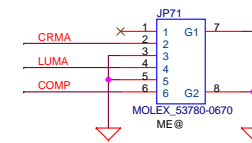


CRT Conn.

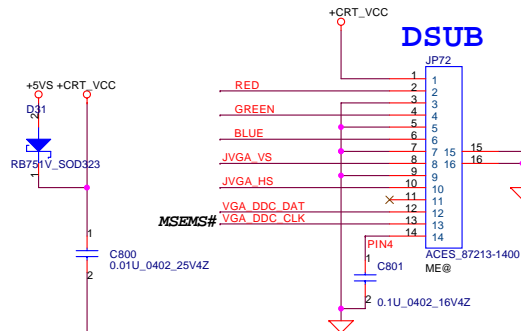


VGA I/O PORT Connector

S-VIDEO

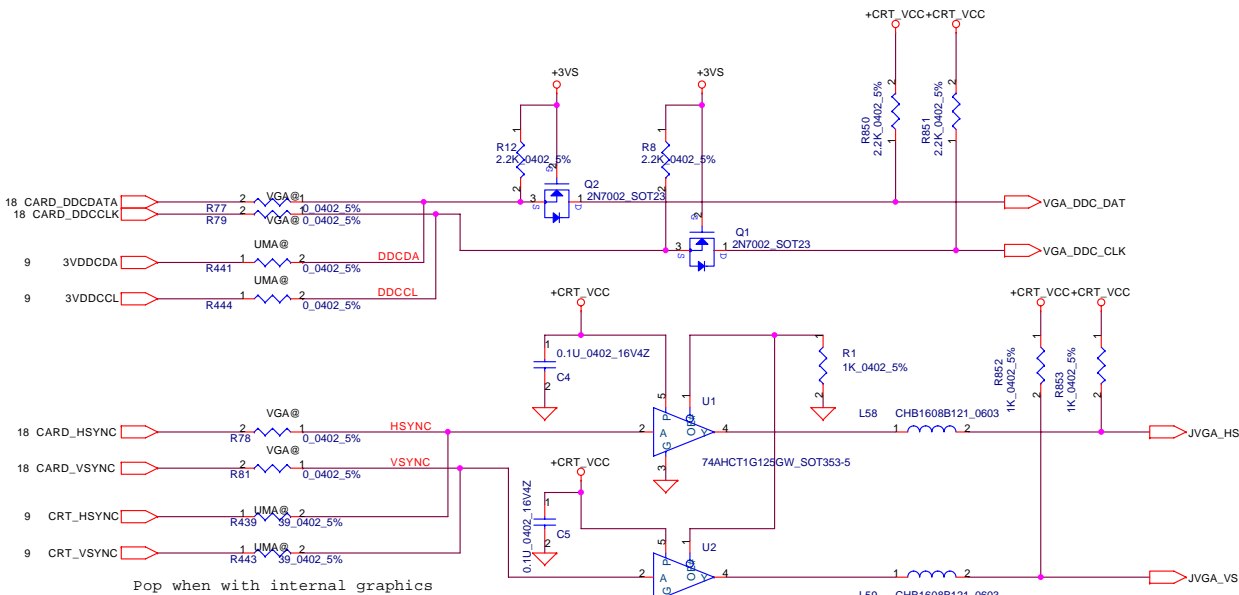


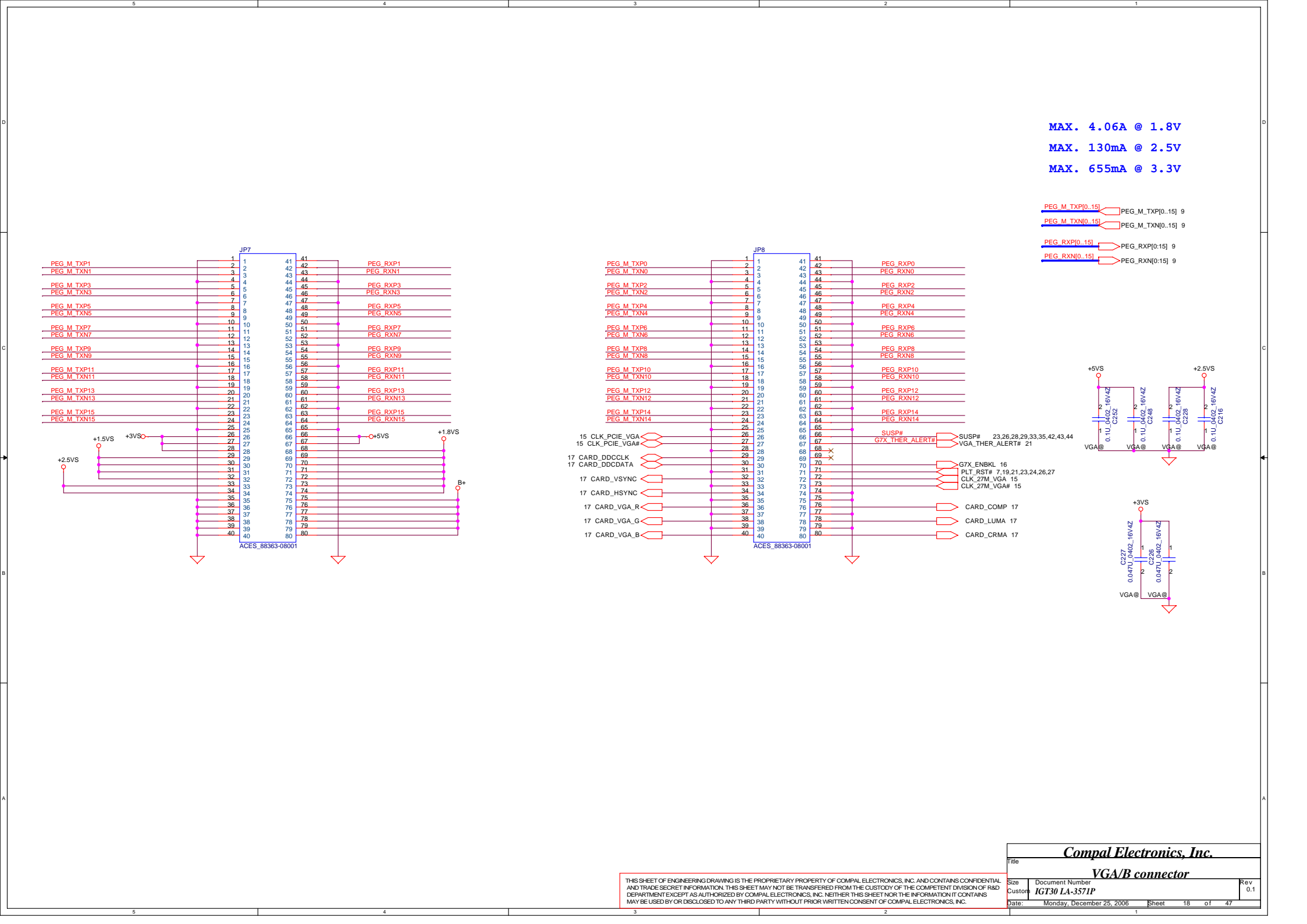
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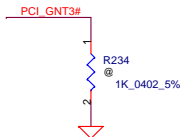
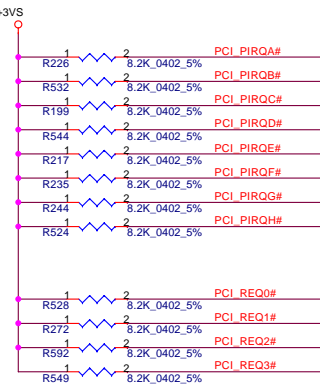
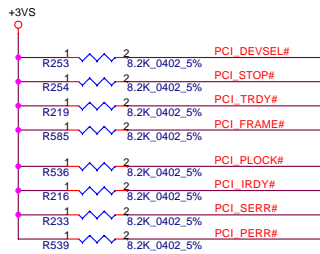


PIN ASSIGMENT

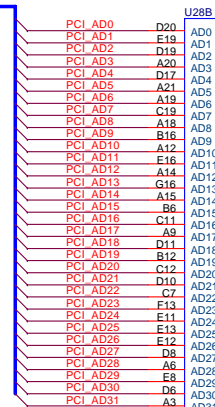
PIN	D-SUB	FUNCTION	PIN	SVIDEO	FUNCTION
1	9	+CART_VCC	1	1	NC
2	1	RED	2	4	CRMA
3	6	GND	3	2	GND
4	2	GREEN	4	3	LUMA
5	7	GND	5	5	GND
6	3	BLUE	6	6	CVBS
7	8	GND			
8	14	VSYS			
9	10	GND			
10	13	HSYNC			
11	11	SENSE			
12	12	SM_DAT			
13	15	SM_CLK			
14	4	PIN4			



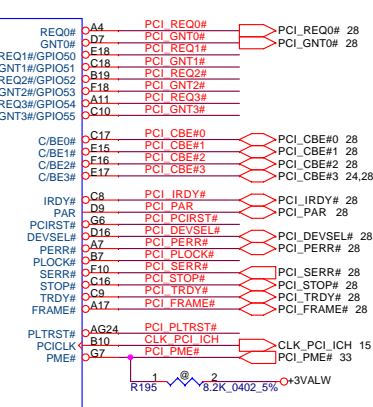




28 PCI_AD[0..31]



PCI



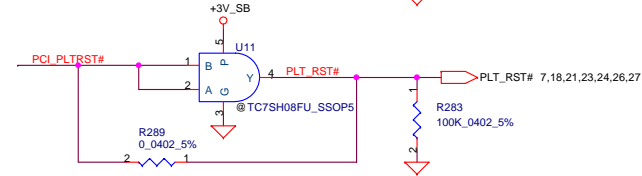
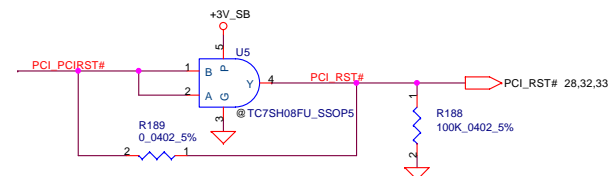
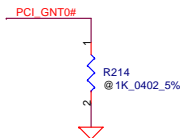
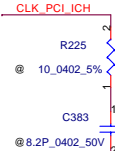
Interrupt I/F

ICH8M REV 1.0

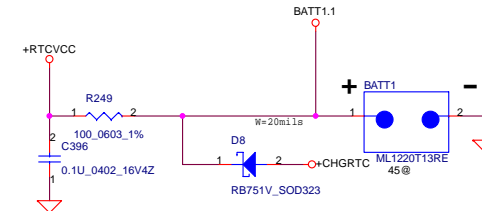
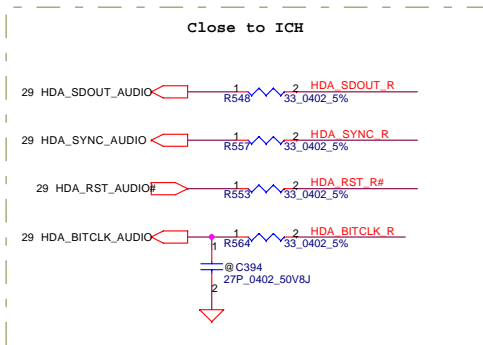
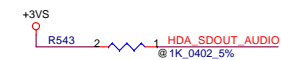
A16 swap override Strap	
PCI_GNT3#	Low= A16 swap override Enable High= Default *

Boot BIOS Strap		
PCI_GNT0#	SPI_CS#1	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC *

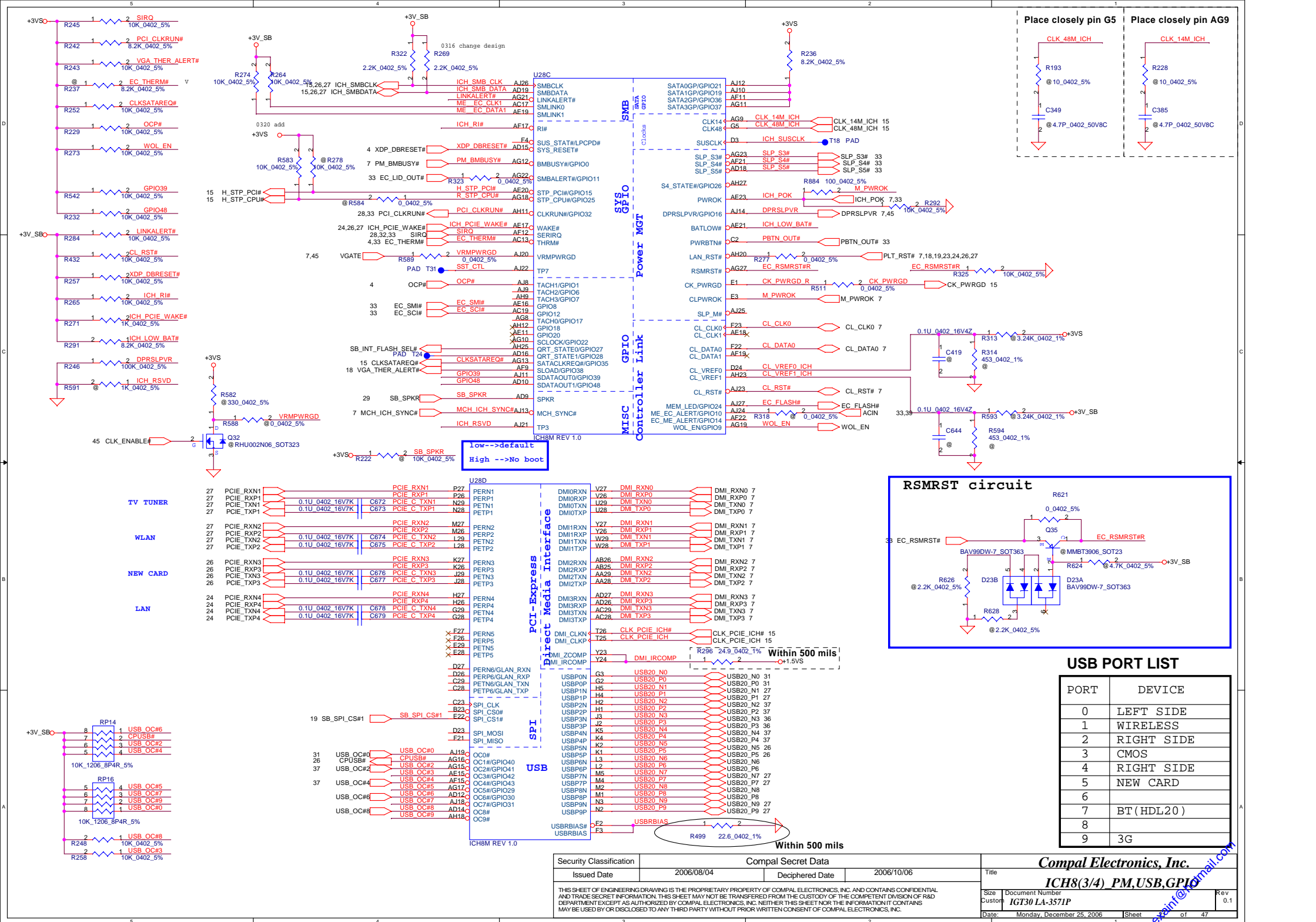
Place closely pin B10

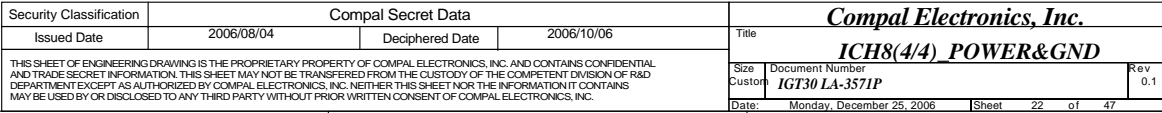


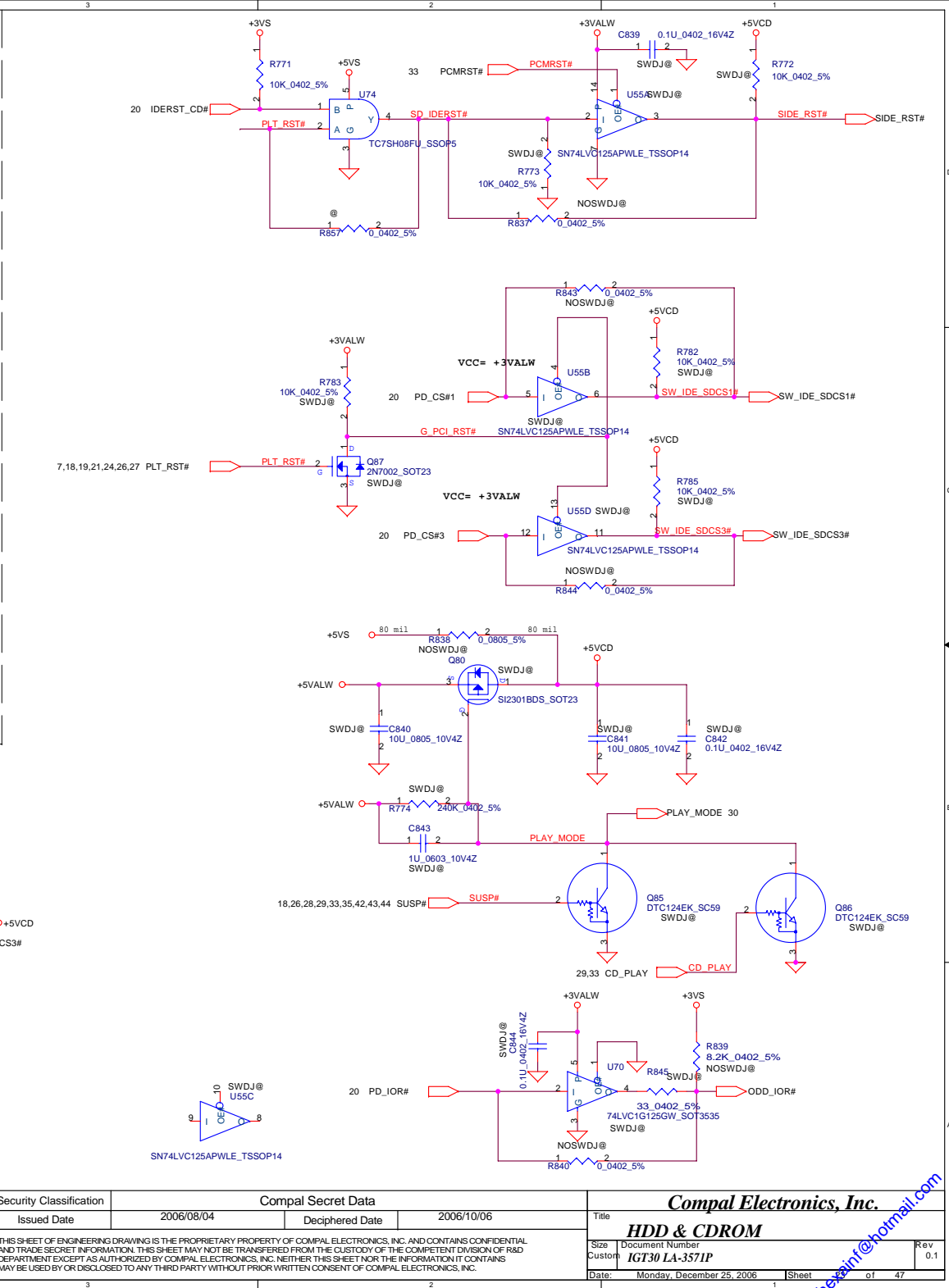
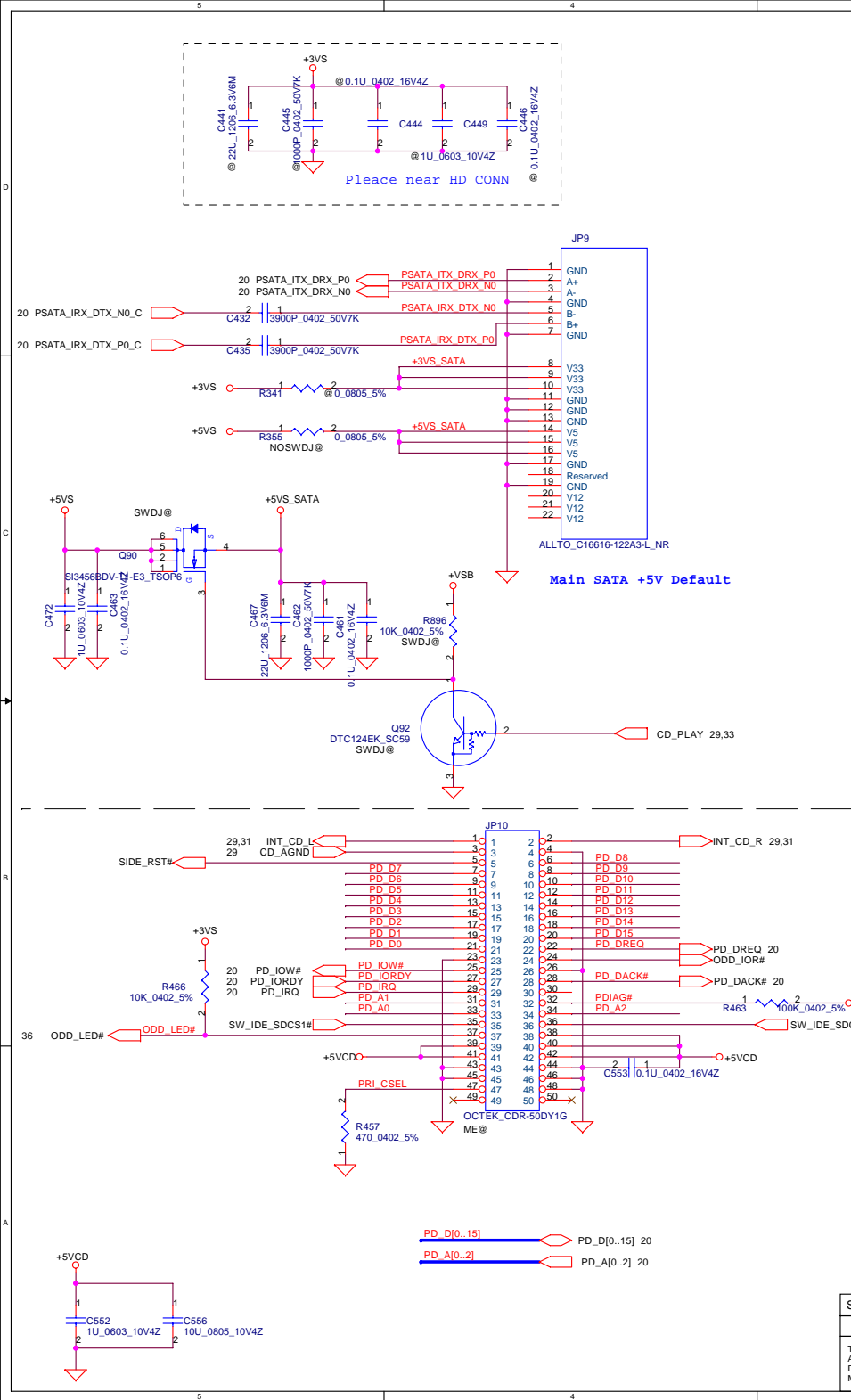
LAN100_SLP	Low = Internal VR Disabled High = Internal VR Enabled(Default)
ICH_INTVRMEN	



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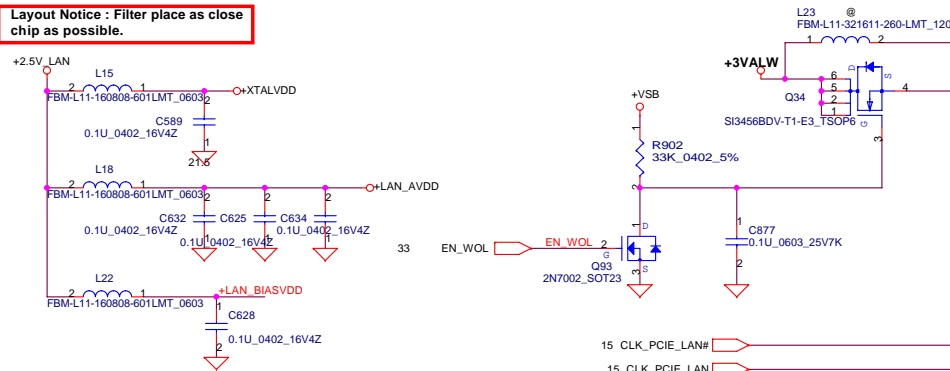
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Issued Date	2006/08/04	Deciphered Date	2006/10/06	Size	Document Number
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				Date	Monday, December 25, 2006
				Sheet	of 47

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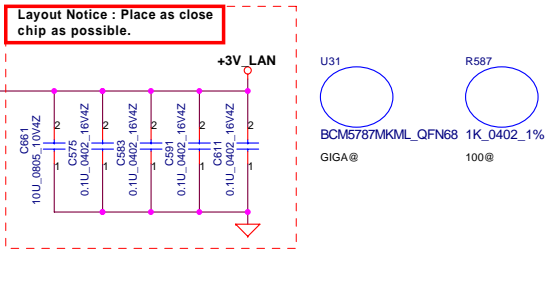
HDD & CDROM

Rev 0.1

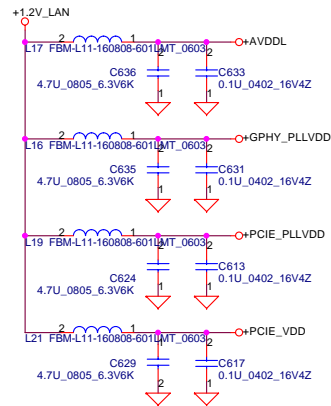
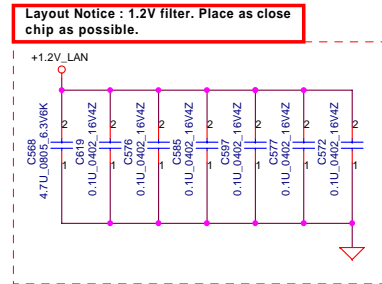
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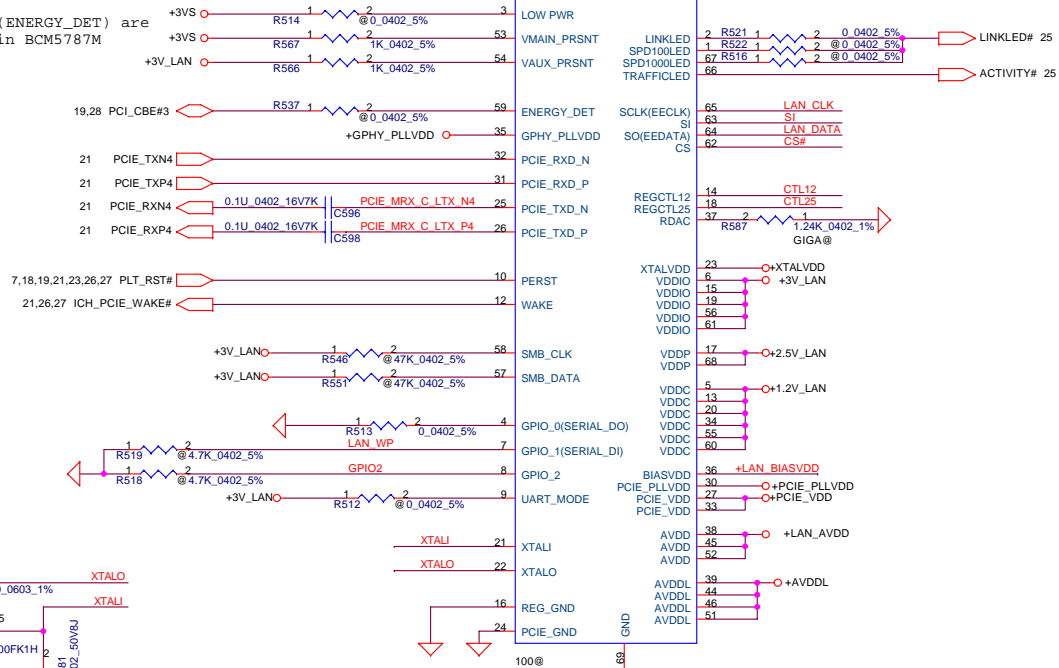
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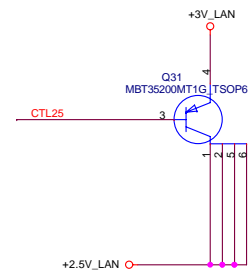
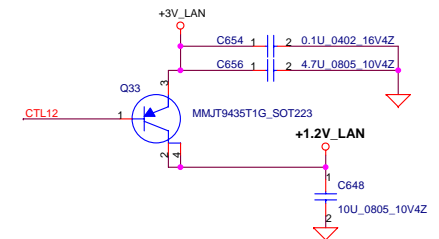
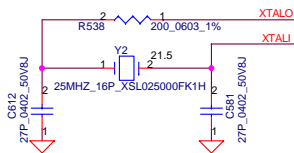
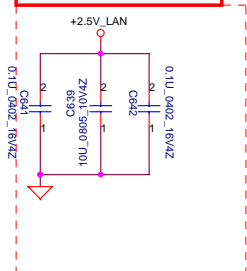
Layout Notice : 1.2V filter. Place as close chip as possible.



(CLKREQ#) and (ENERGY_DET) are only supported in BCM5787M

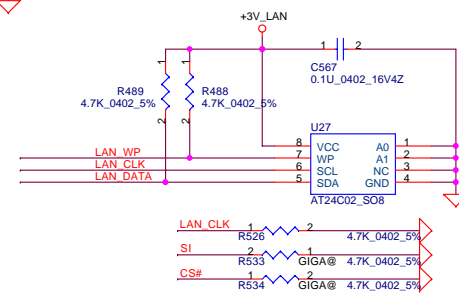


**Layout Notice : Place as close
chip as possible.**

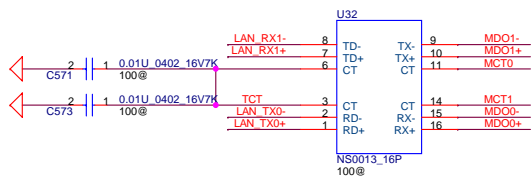


Notice : 4.7u 6.3V capactor Thickness 1.25mm

Layout Notice : Filter place as close chip as possible.

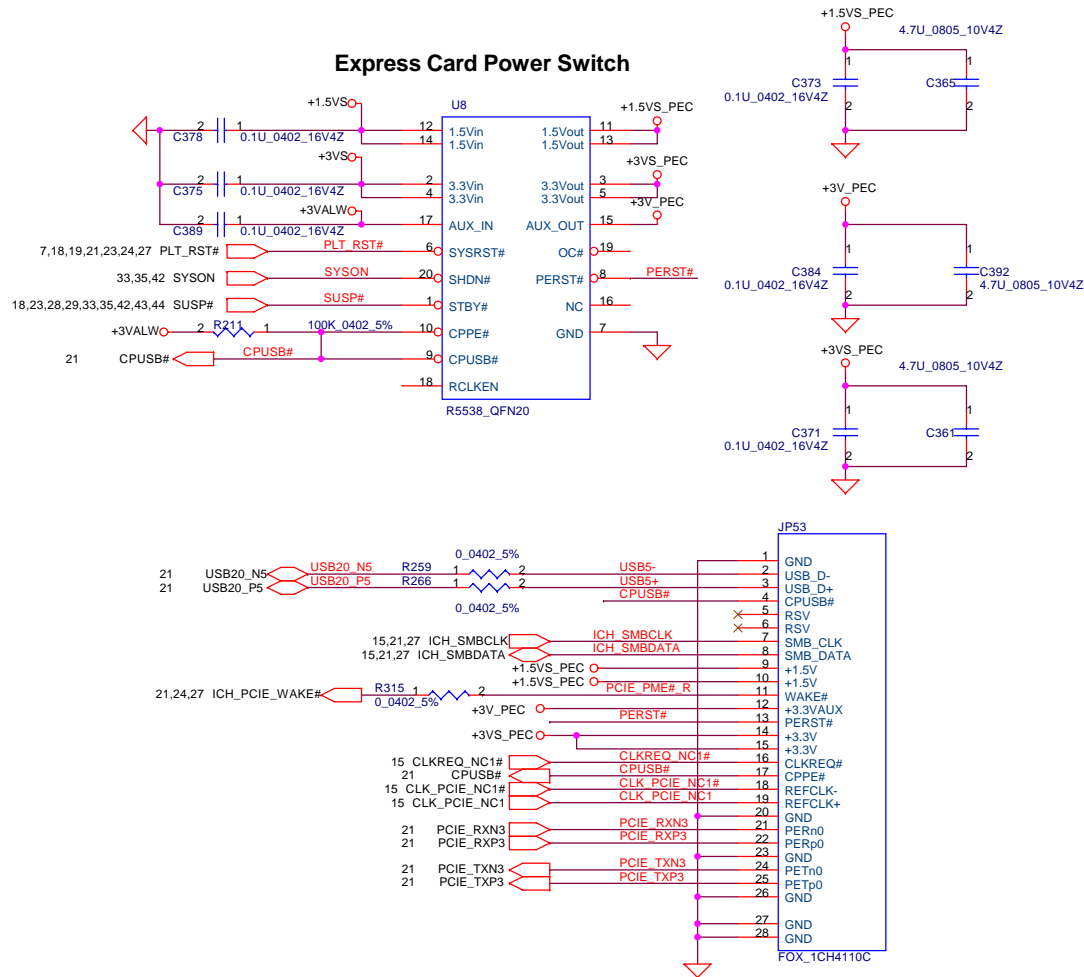


Security Classification		Compal Secret Data						
Issued Date	2006/08/04	Deciphered Date	2006/10/06	Title				
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				Custn	IGT30 LA-3571P		0.1	
				Date:	Monday, December 25, 2006	Sheet	24 of 47	

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Issued Date		2006/08/04	Deciphered Date	2006/10/06	Title LAN CONTROLLER
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				Customer	IGT30 LA-351P
Date:		Monday, December 25, 2006		Sheet	47 of 47

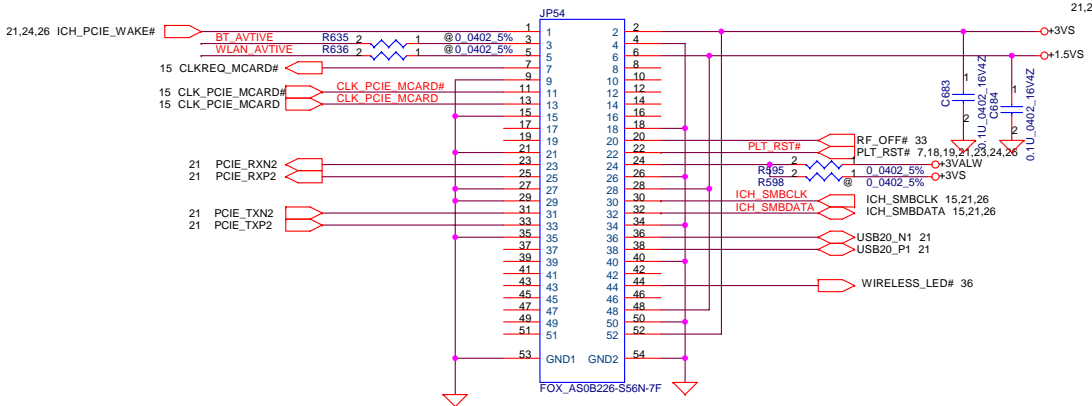
Express Card Power Switch



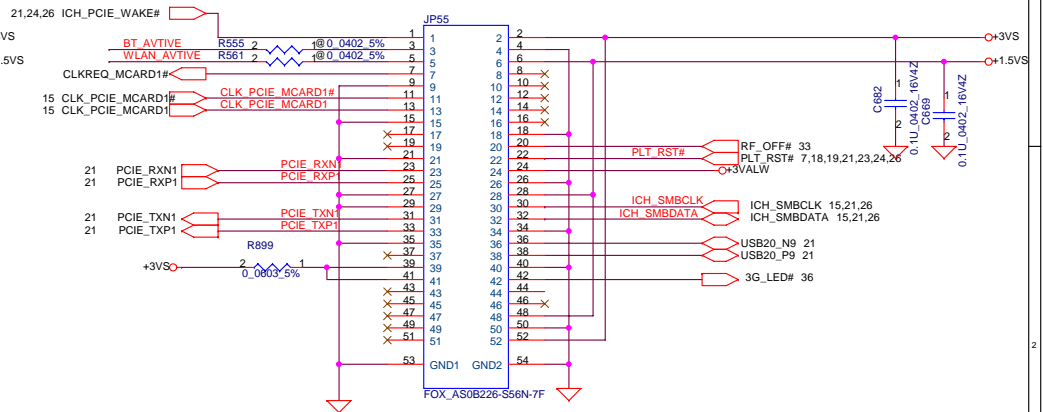
Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2006/08/04		Deciphered Date		2006/10/06		Title	
										CardBus Socket	
										Size	
										Document Number	
										IGT30 LA-3571P	
										Rev	
										0.1	
										Date:	
										Monday, December 25, 2006	
										Sheet	
										26 of 47	
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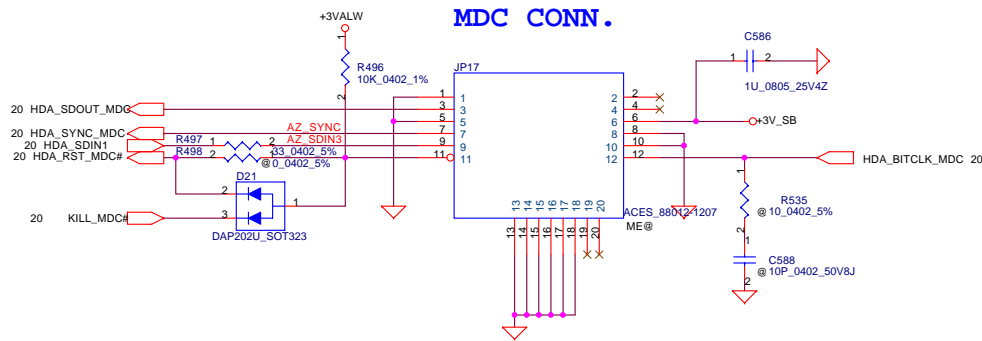
Mini-Express Card(Slot 1-WLAN)



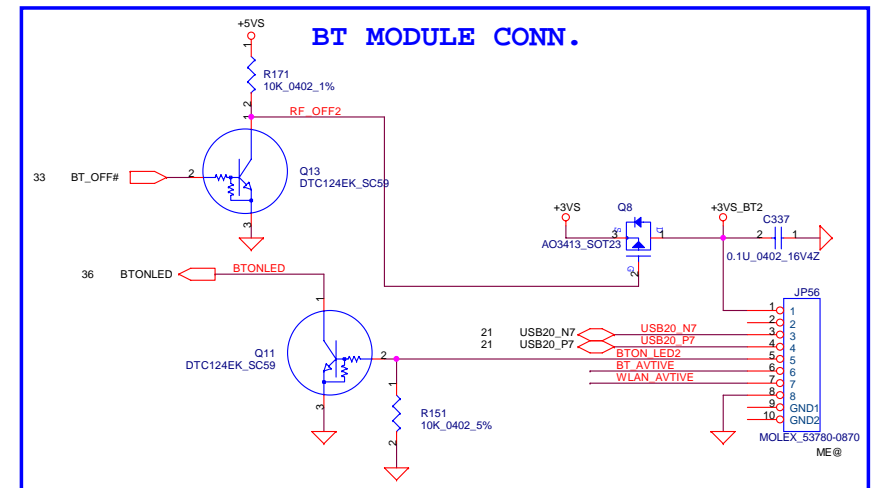
Mini-Express Card(Slot 2-3G)



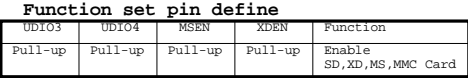
MDC CONN.

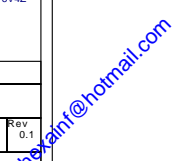


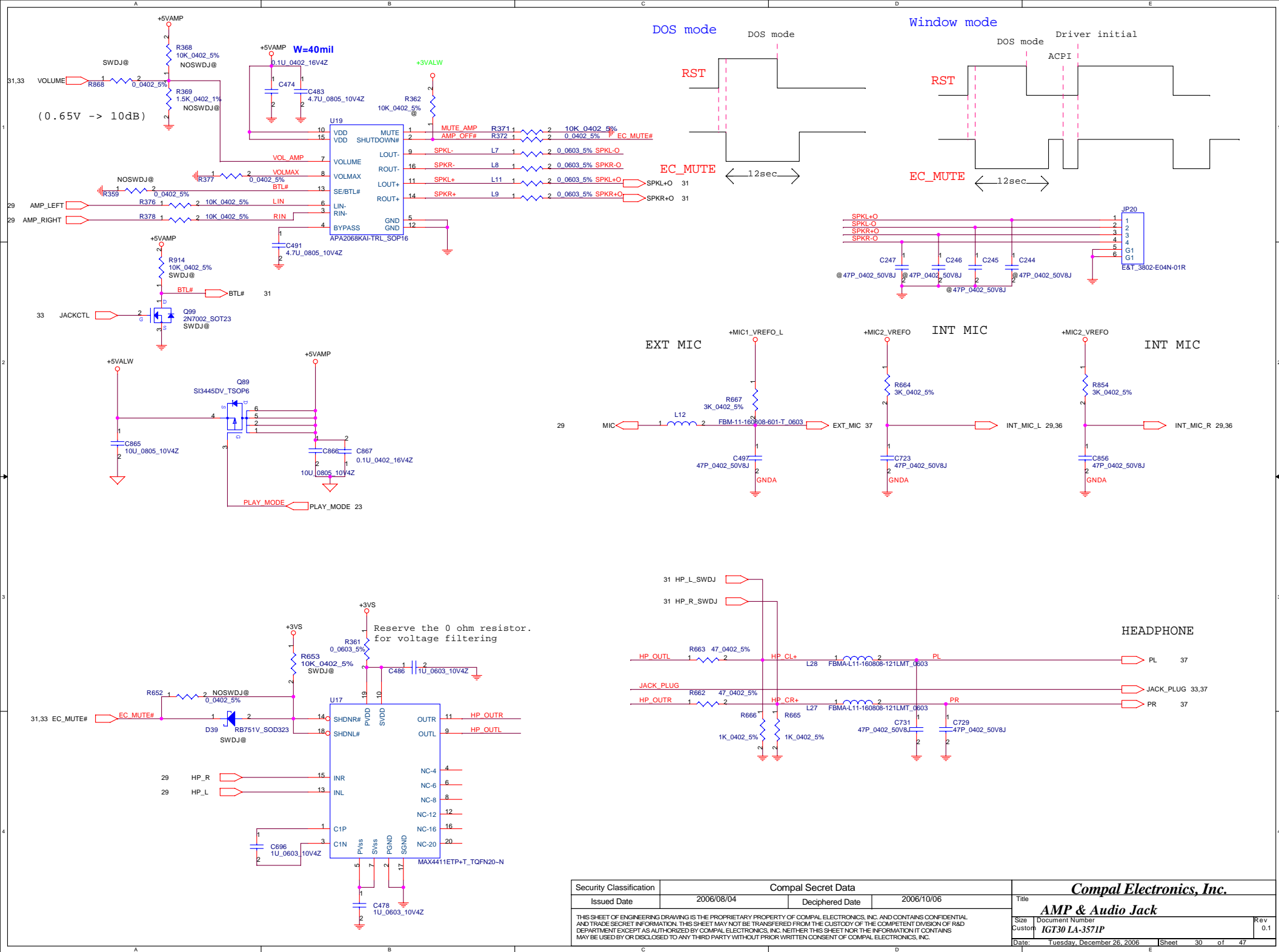
BT MODULE CONN.



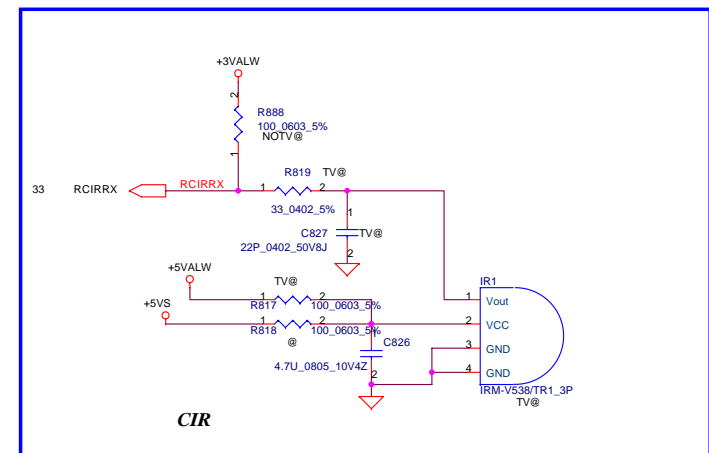
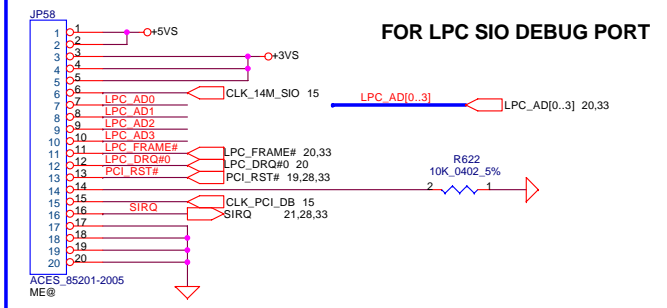
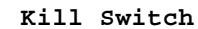
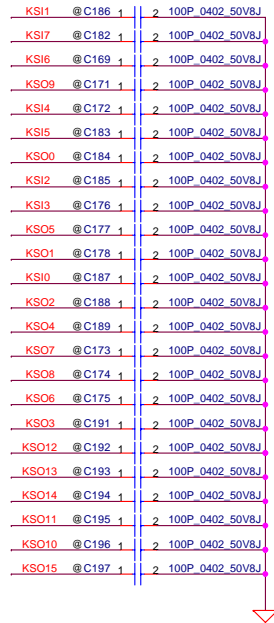
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Issued Date		2006/08/04	Deciphered Date	2006/10/06		Title	
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				Size	Document Number		Rev
				Custom	IGT30 LA-3571P		0.1
				Date:	Monday, December 25, 2006	Sheet	of 47



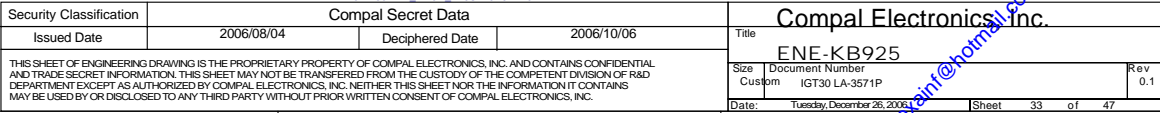


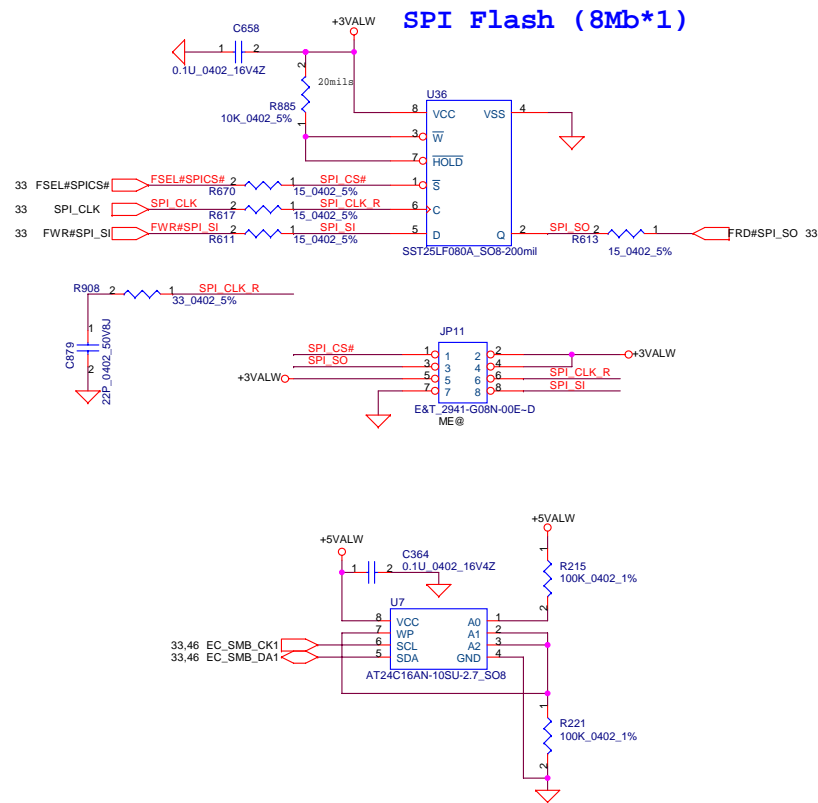


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Issued Date	2006/08/04	Deciphered Date	2006/10/06	Title	Bluetooth & USB CONN.		
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				Customs	IGT30 LA-3571P	0.1	
				Date:	Tuesday, December 26, 2006	ISheet	31 of 47

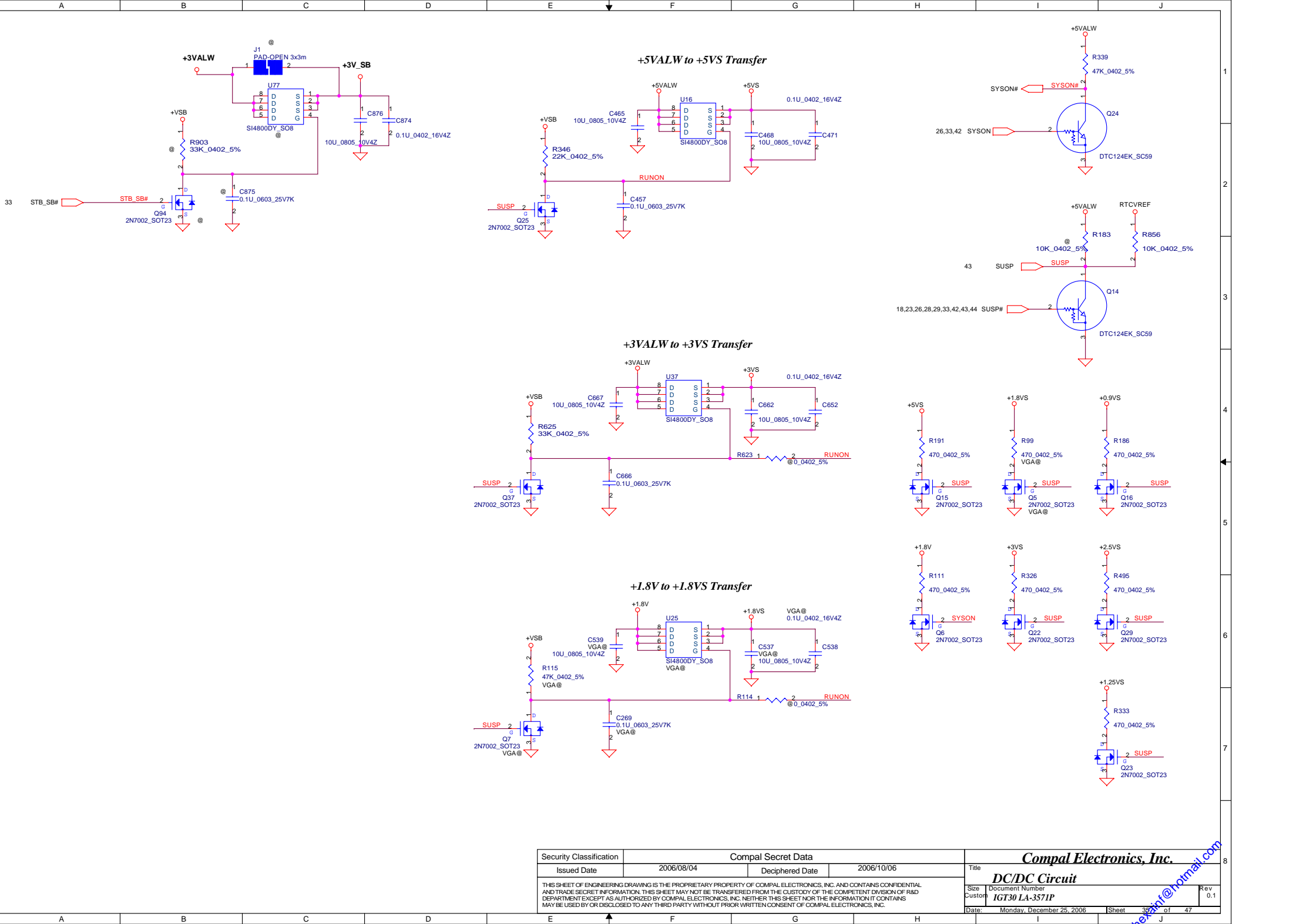


Security Classification		Compal Secret Data		Compal Electronics, Inc. KBD,ON/OFF,T/P,LED/B,DEBUG	
Issued Date	2006/08/04	Deciphered Date	2006/10/06	Title KBD,ON/OFF,T/P,LED/B,DEBUG	Size Document Number IGT30 LA-3571P
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Date:		Monday, December 25, 2006		Sheet	32 of 47



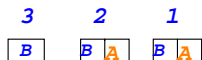


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Issued Date	2006/08/04	Deciphered Date	2006/10/06	Title	
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				Size	Rev
				Custom	0.1
				Date:	Monday, December 25, 2006
				Sheet	34 of 47



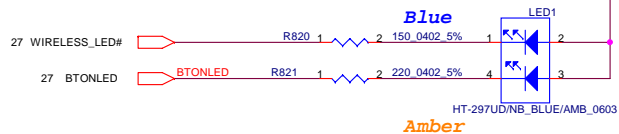
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/08/04	Deciphered Date	2006/10/06	Title	DC/DC Circuit
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LED Indicator ON M/B



Wireless / Bluetooth LED

Blue

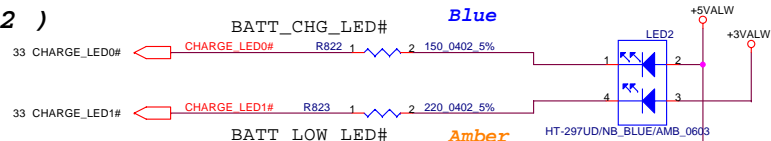


Amber

STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

BATT_CHG_LED#

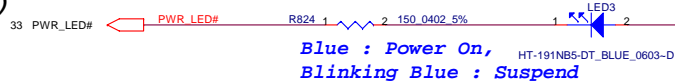
Blue



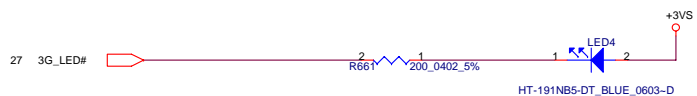
BATT_LOW_LED#

Amber

(3)

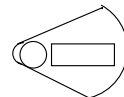


Blue : Power On,
Blinking Blue : Suspend



STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

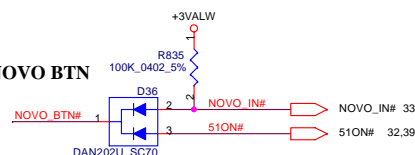
Dial Wheel



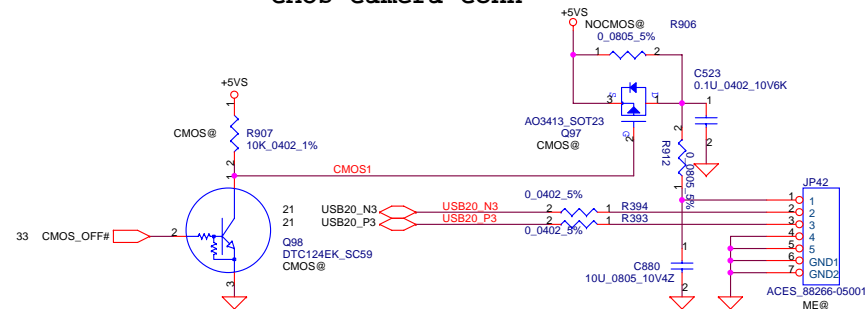
Function

KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

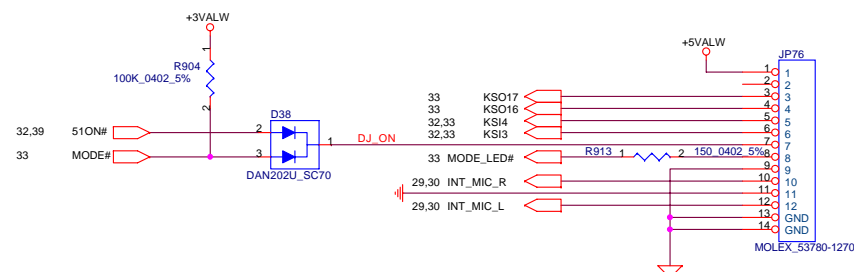
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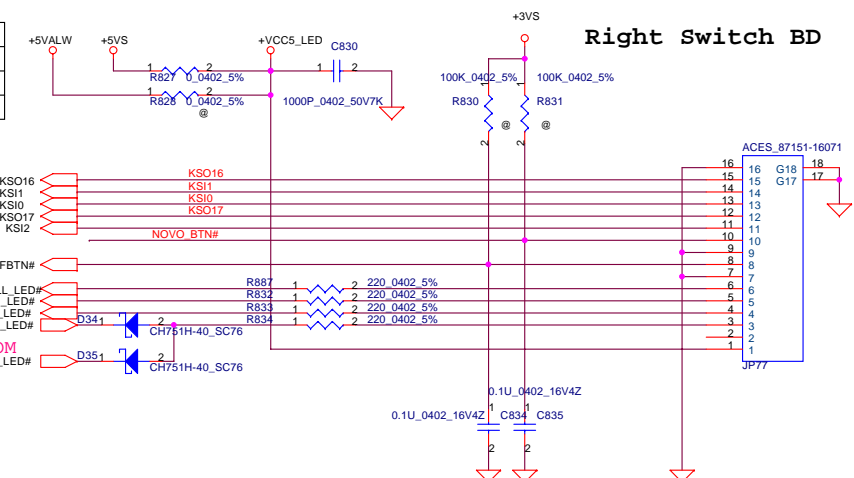
CMOS Camera Conn



Left Switch BD(AUDIO DJ)



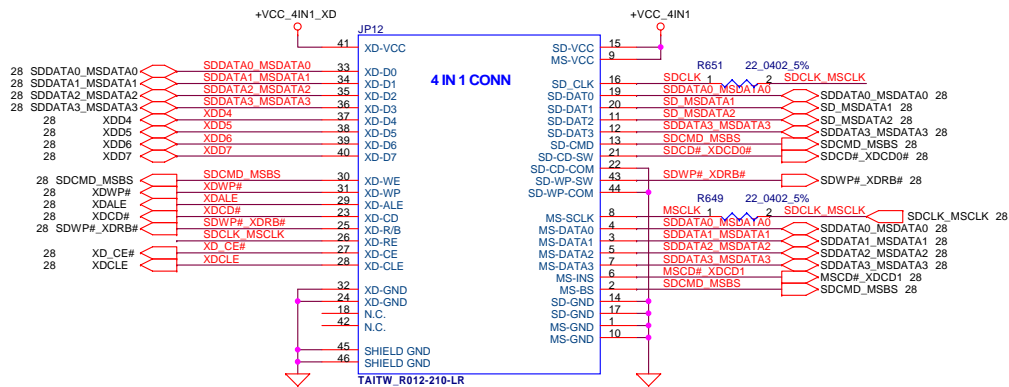
Right Switch BD



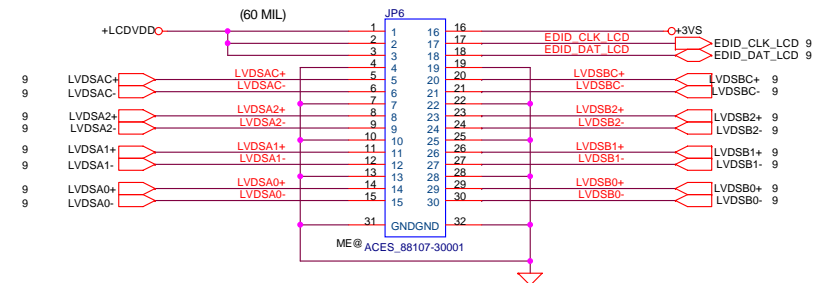
C839, C841, C842 For EMI Solution

Security Classification	Compal Secret Data		Title	
Issued Date	2006/08/04	Deciphered Date	2006/10/06	INDICATE LED
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Date:	Monday, December 25, 2006	Sheet	36 of 47	

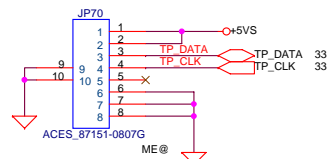
4 in 1 Card Reader



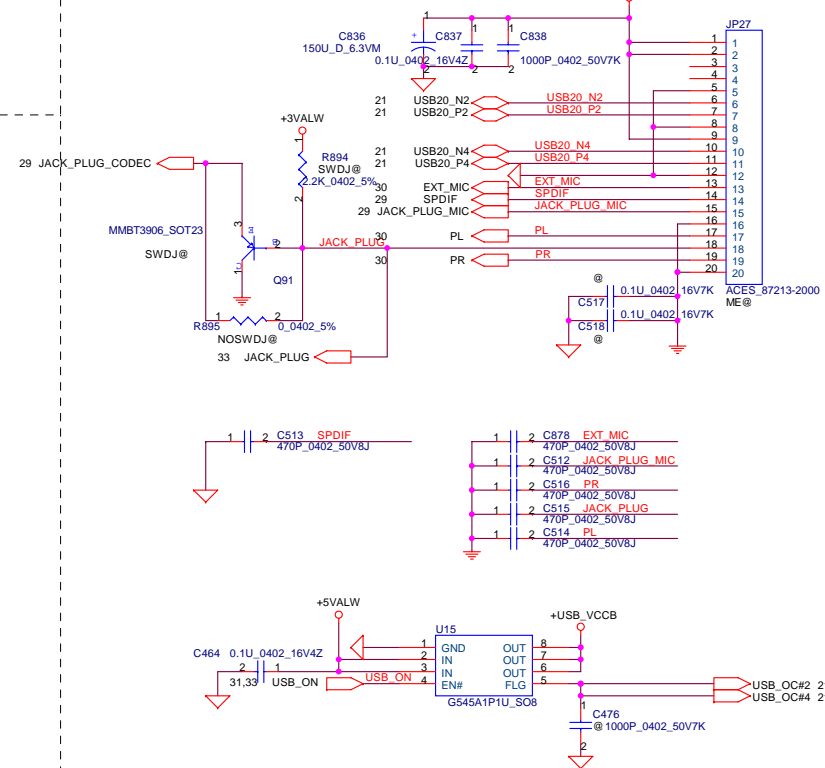
UMA LCD/PANEL Conn.



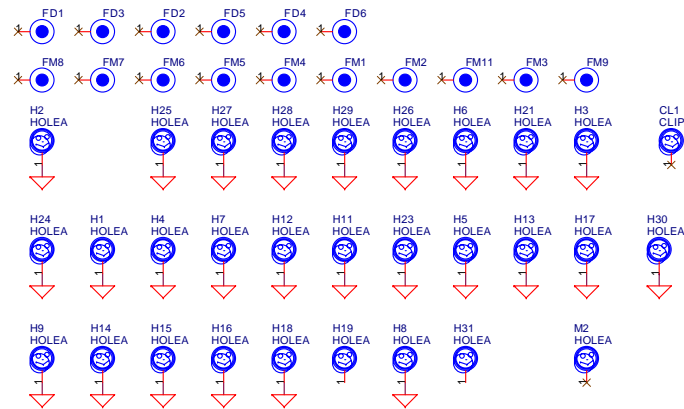
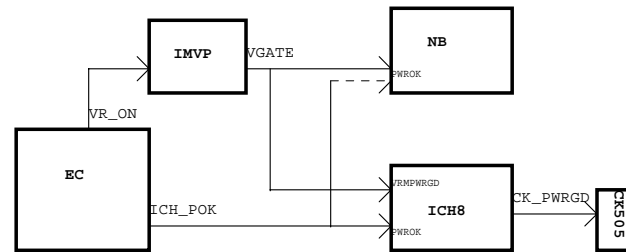
T/P Board



Audio Jack/USB Conn.



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				Custom	IGT30 LA-3571P
				Date	Monday, December 25, 2006
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ACIN

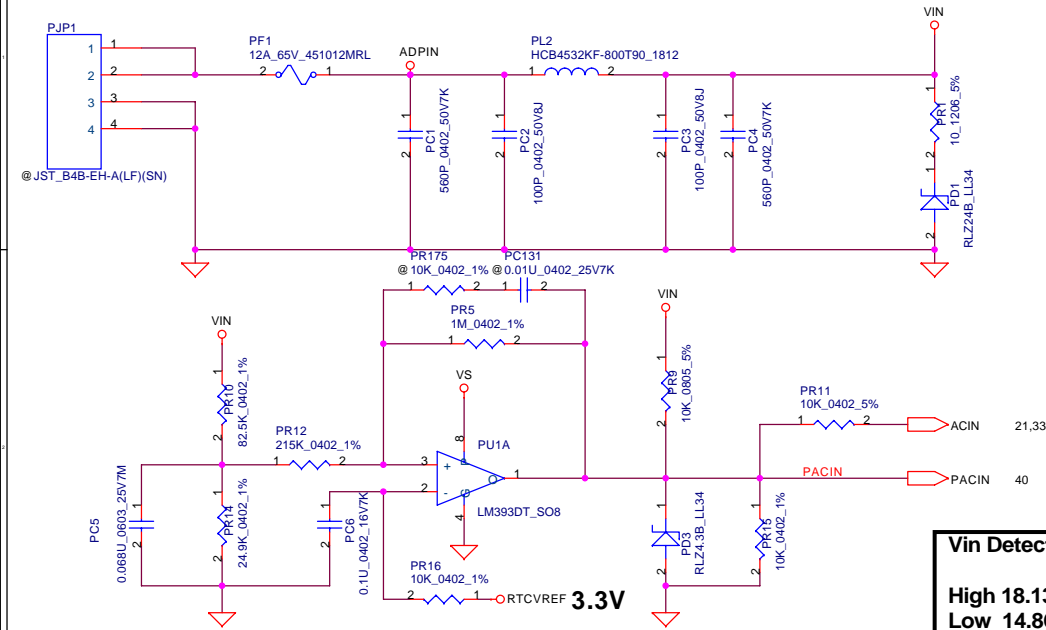
Precharge detector

	Min.	typ.	Max.
H->L	13.843V	14.247V	14.636V
L->H	14.936V	15.381V	15.814V

BATT ONLY

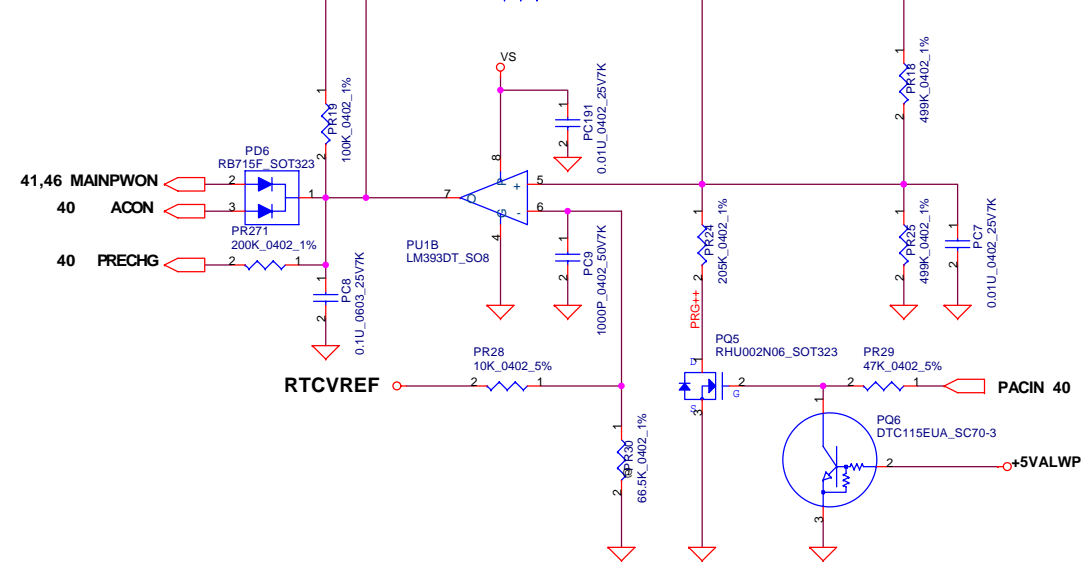
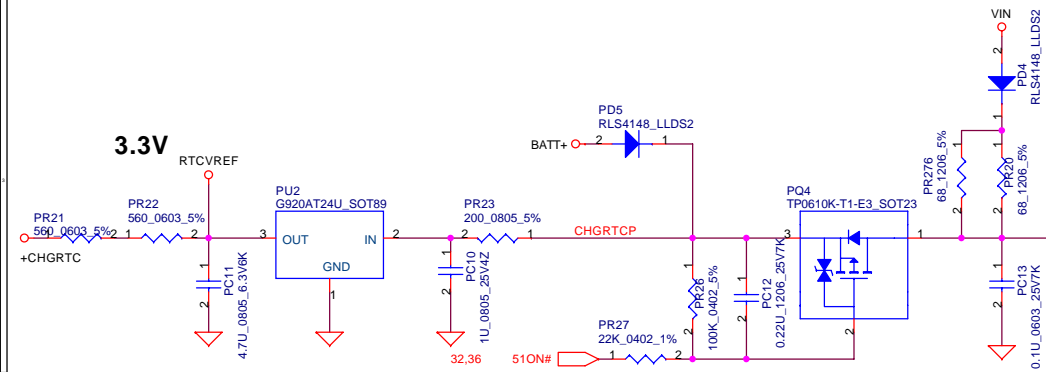
Precharge detector

	Min.	typ.	Max.
H->L	6.138V	6.214V	6.359V
L->H	7.196V	7.349V	7.505V

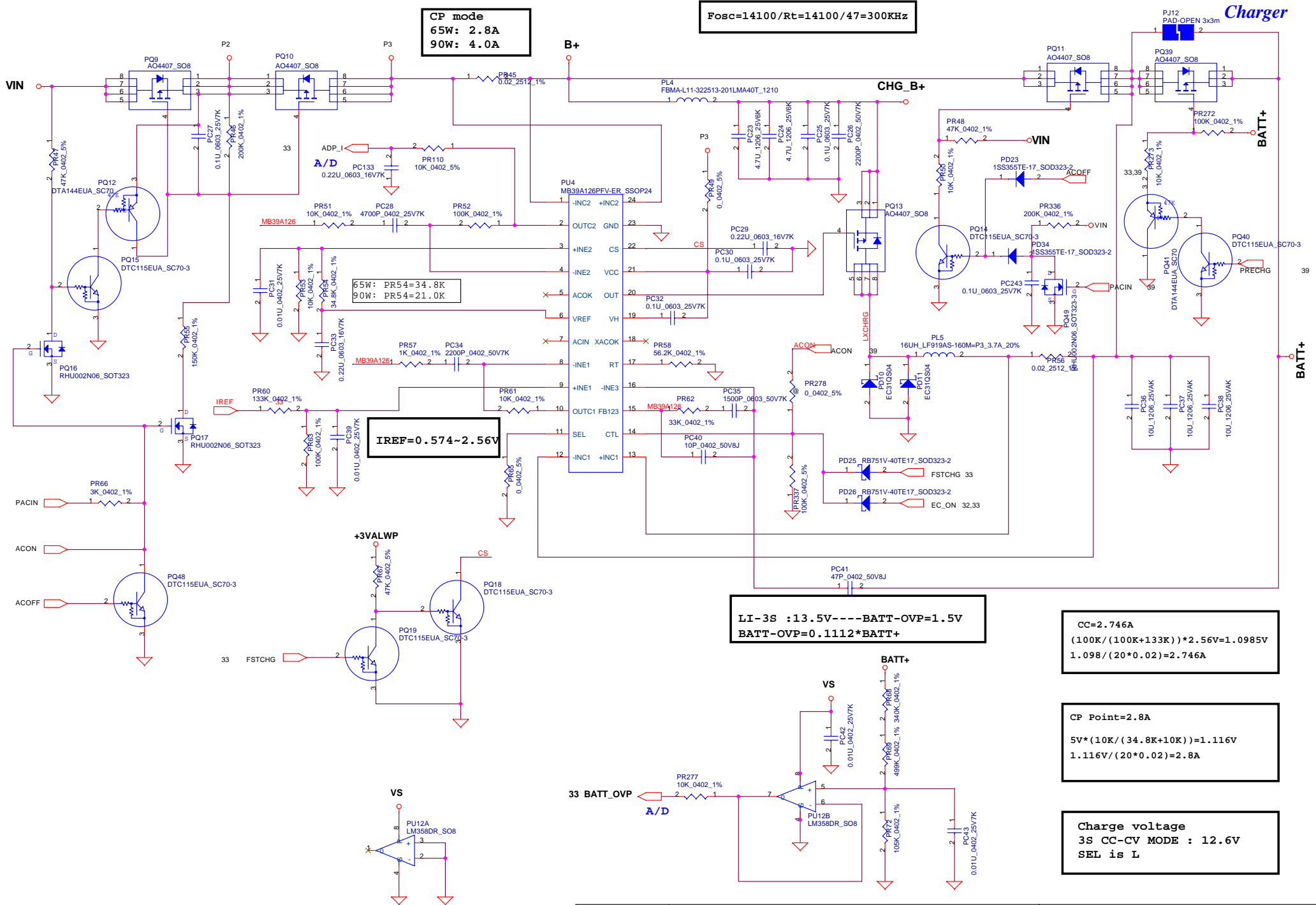


Vin Detector

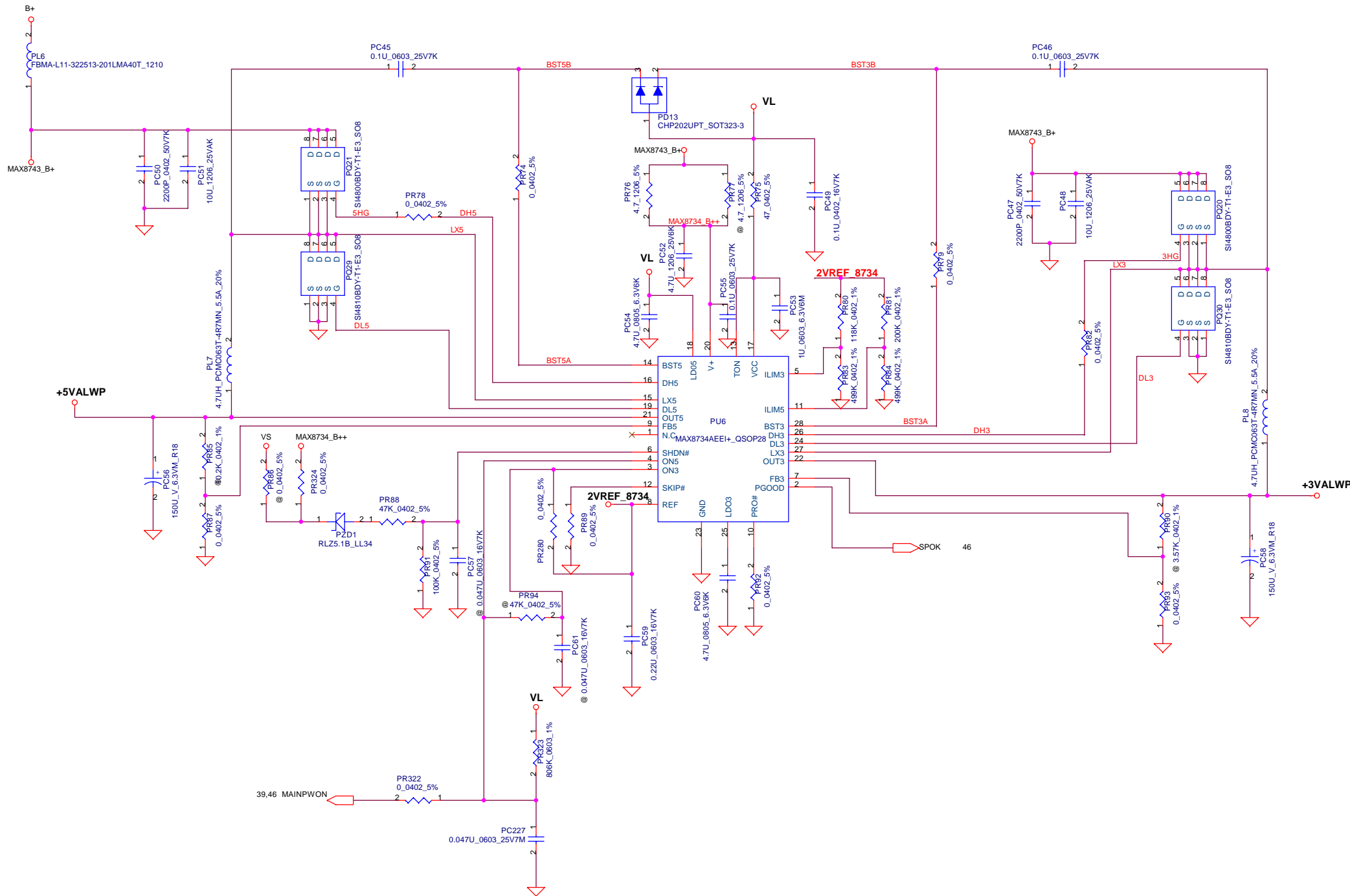
	High	18.135	17.566	17.011
	Low	14.866	14.355	14.063



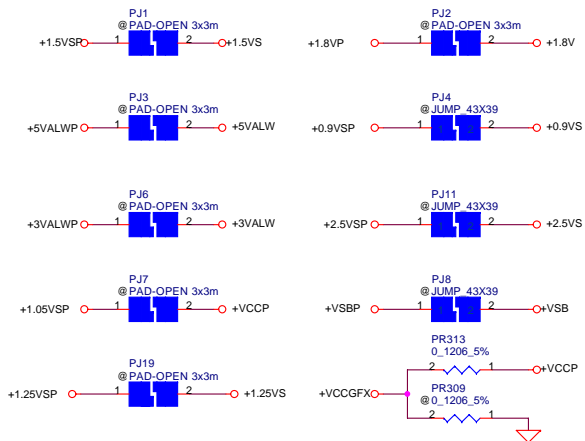
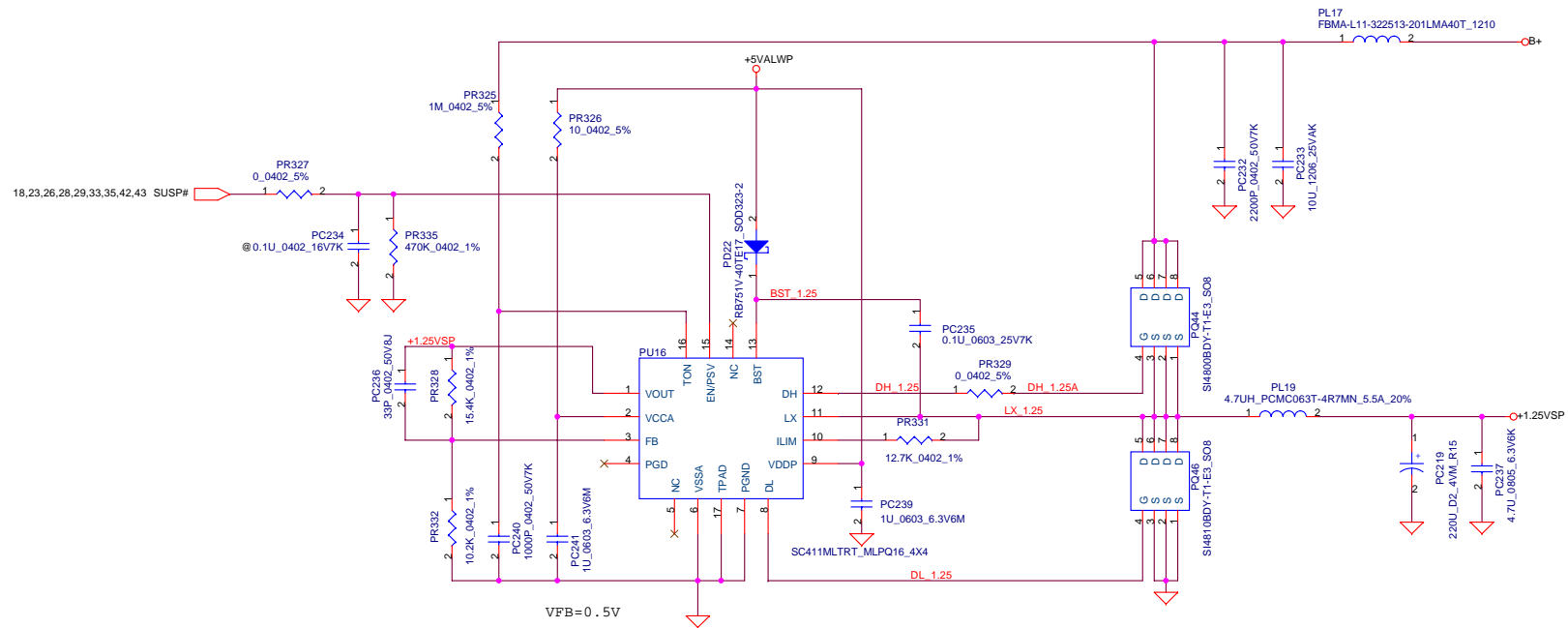
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Issued Date	2006/08/04	Deciphered Date	2006/10/06	Title	DCIN/DETECTOR
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Custom	Custom	0.1		Monday, December 25, 2006	
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UMA Discrete PR313 PR309

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